

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No.09/167,174
Priority Filing Date October 6, 1998
Inventor..... Aftab Ahmad, et al.
Assignee..... Micron Technology, Inc.
Priority Group Art Unit2811
Priority ExaminerO. Nadav
Attorney's Docket No.MI22-1885
Title: Semiconductor Transistor Devices and Methods for Forming Semiconductor
Transistor Devices

Assistant Commissioner for Patents
Washington, D. C. 20231
Attention: Official Draftsman

LETTER SUBMITTING FORMAL DRAWINGS

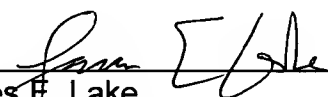
Please enter the enclosed formal drawings in the above-referenced application in place of the originally filed drawings.

The proposed changes are indicated in red on attached copies. The formal drawings incorporate the proposed changes. Acknowledgment of receipt of the formal drawing sheets and their acceptance into the file is requested.

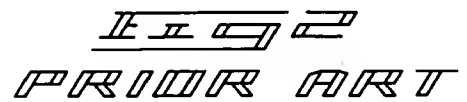
Respectfully submitted,

Date: 29 Nov 2001

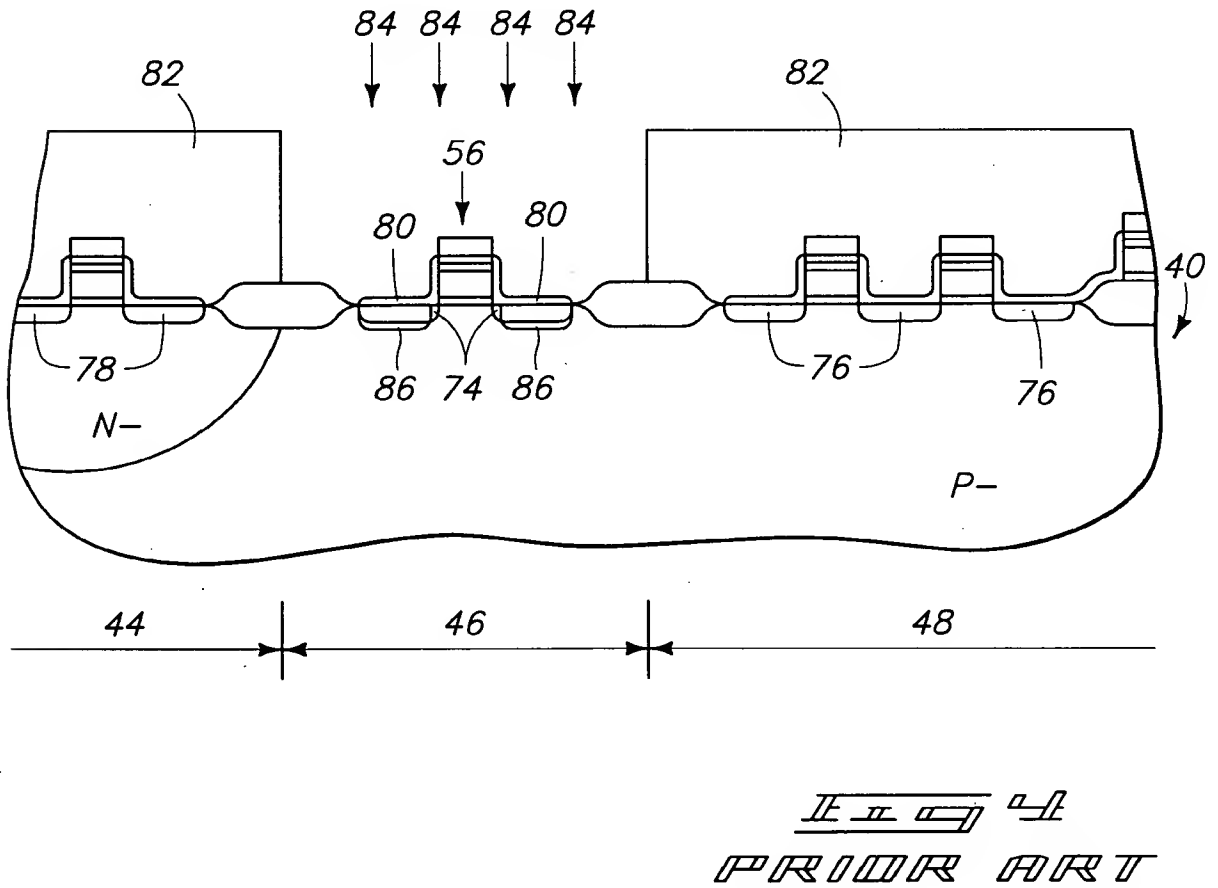
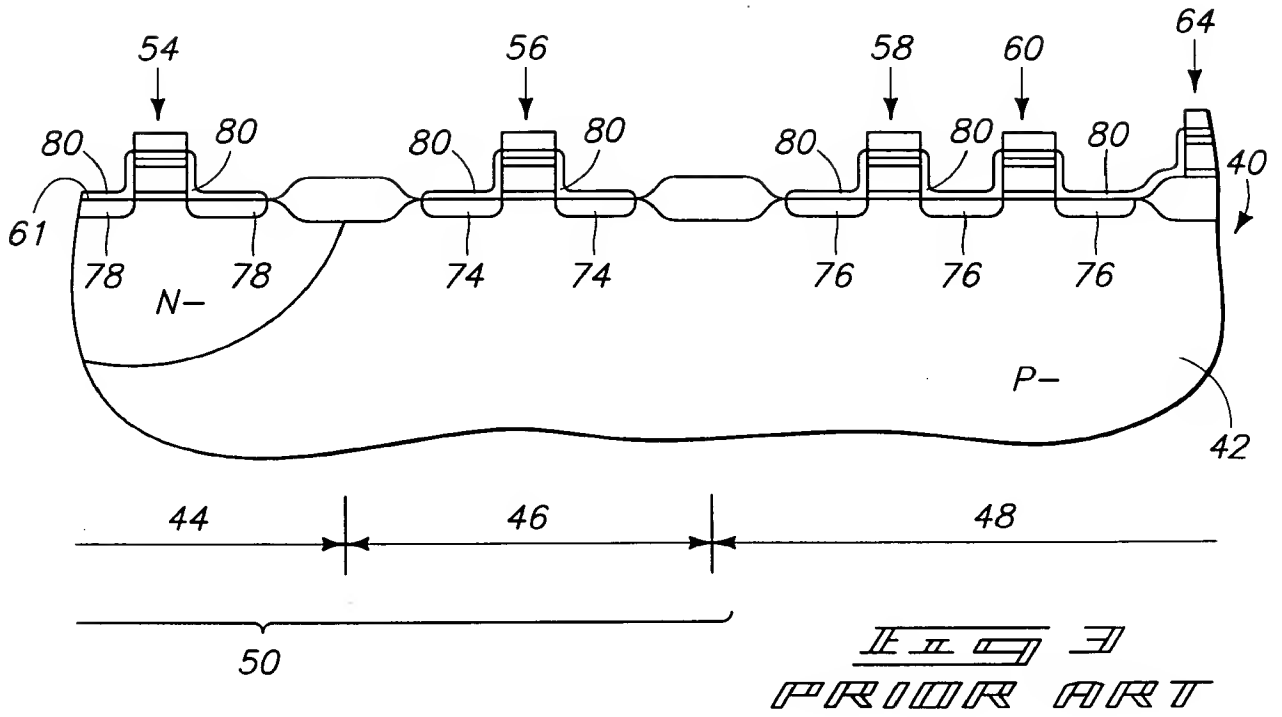
By:


James E. Lake
Reg. No.: 44,854

Enclosures: Twelve (12) sheets of redline drawings and Twenty (20) sheets of formal drawings, Figs. 1-38



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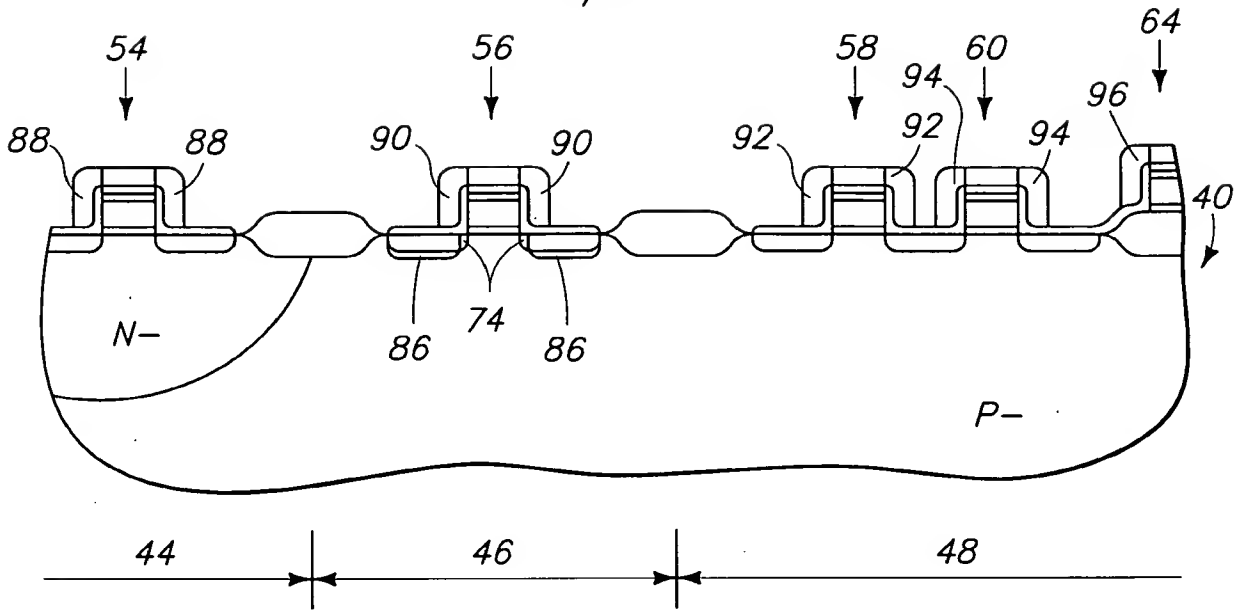


FIG 5
PRIOR ART

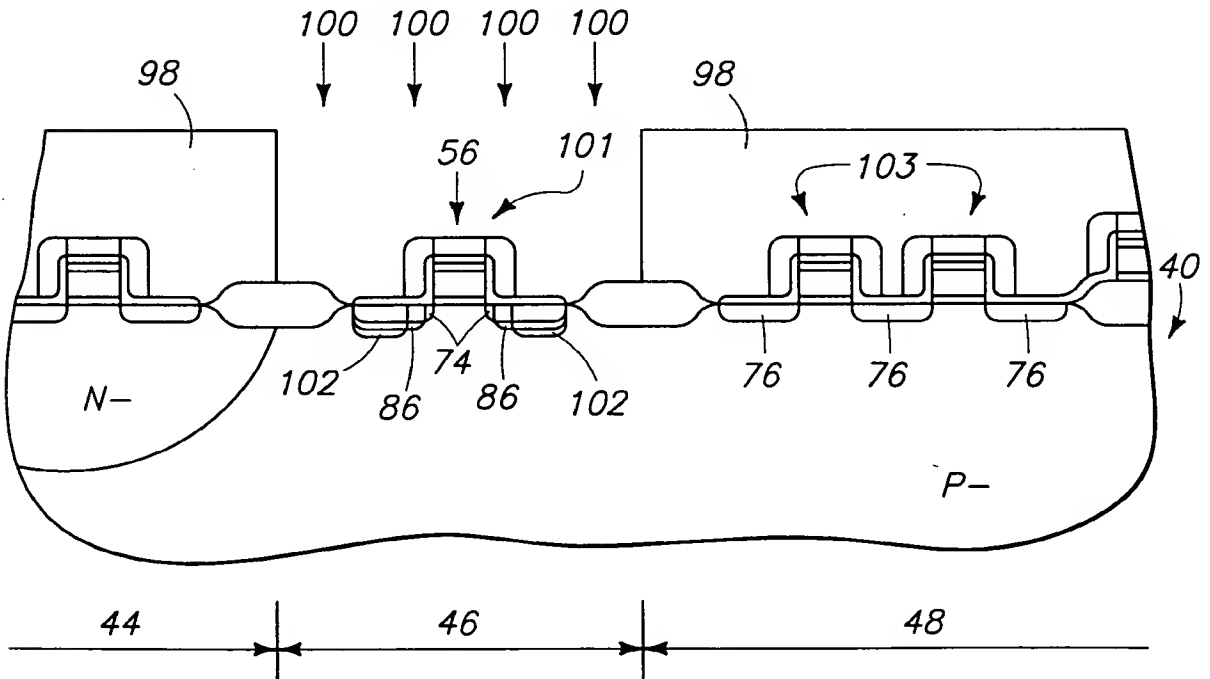


FIG 6
PRIOR ART

FIG. 5

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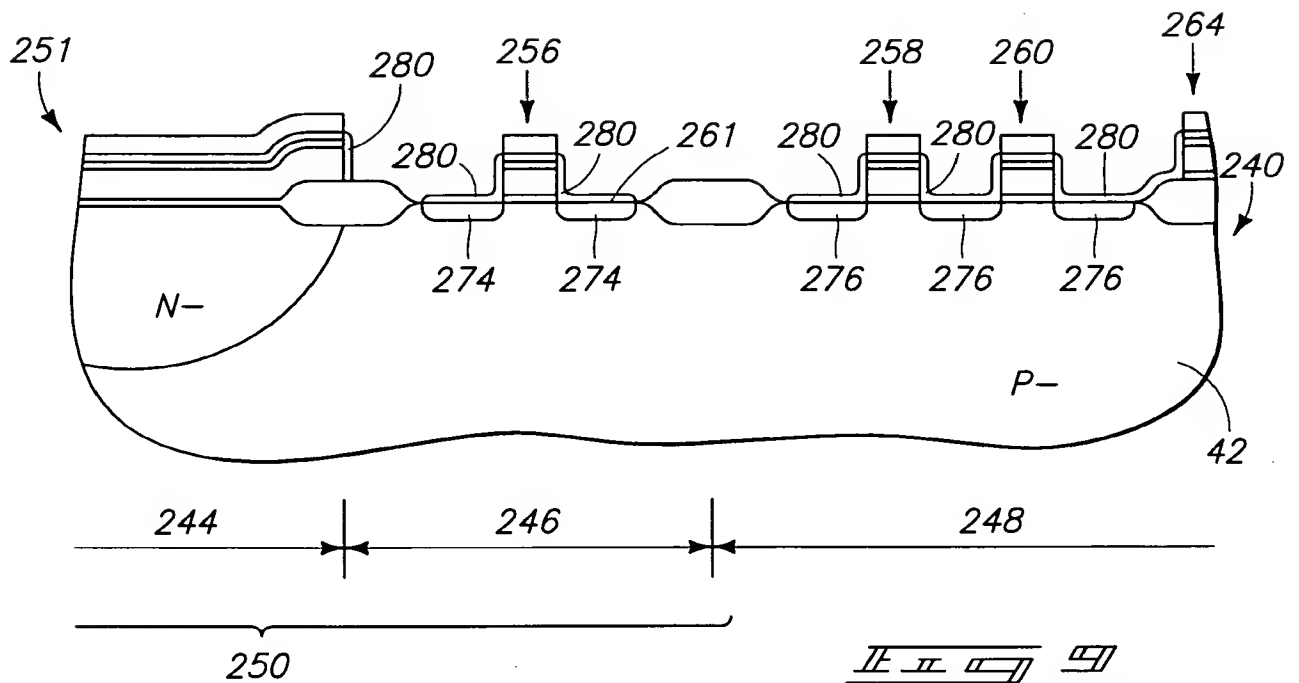


FIG. 1
PRIOR ART

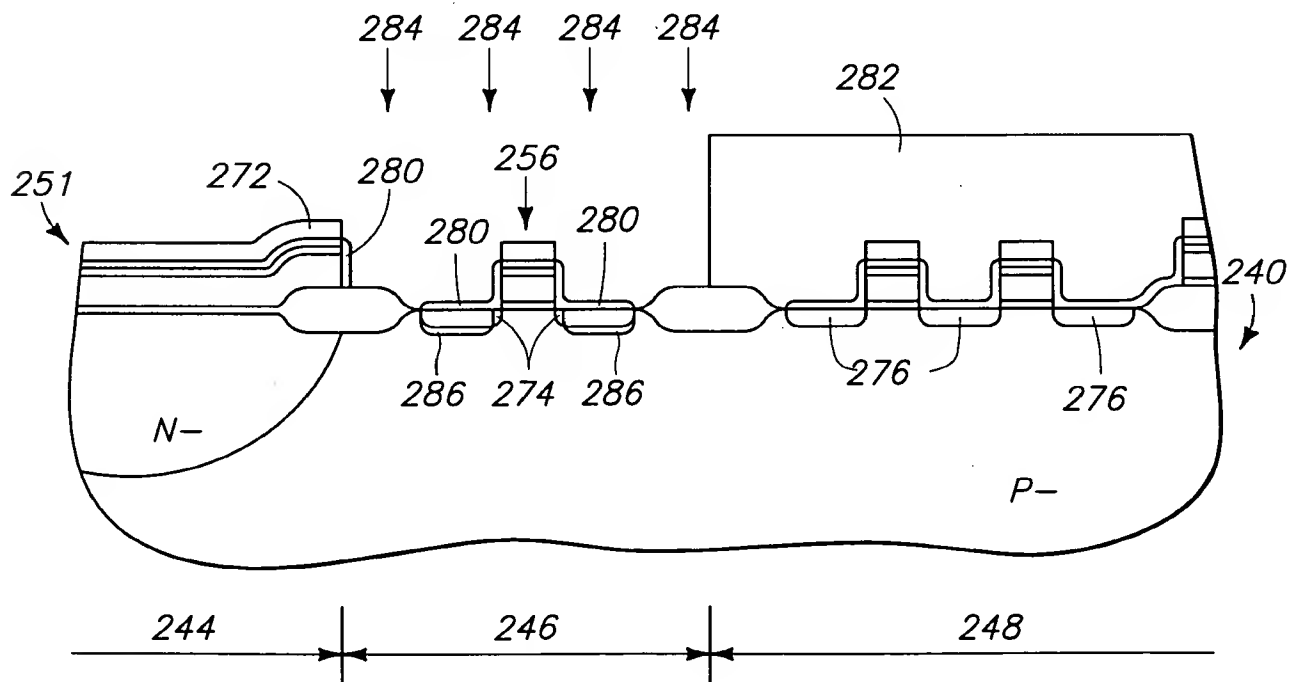


FIG. 2
PRIOR ART

FIG. 1

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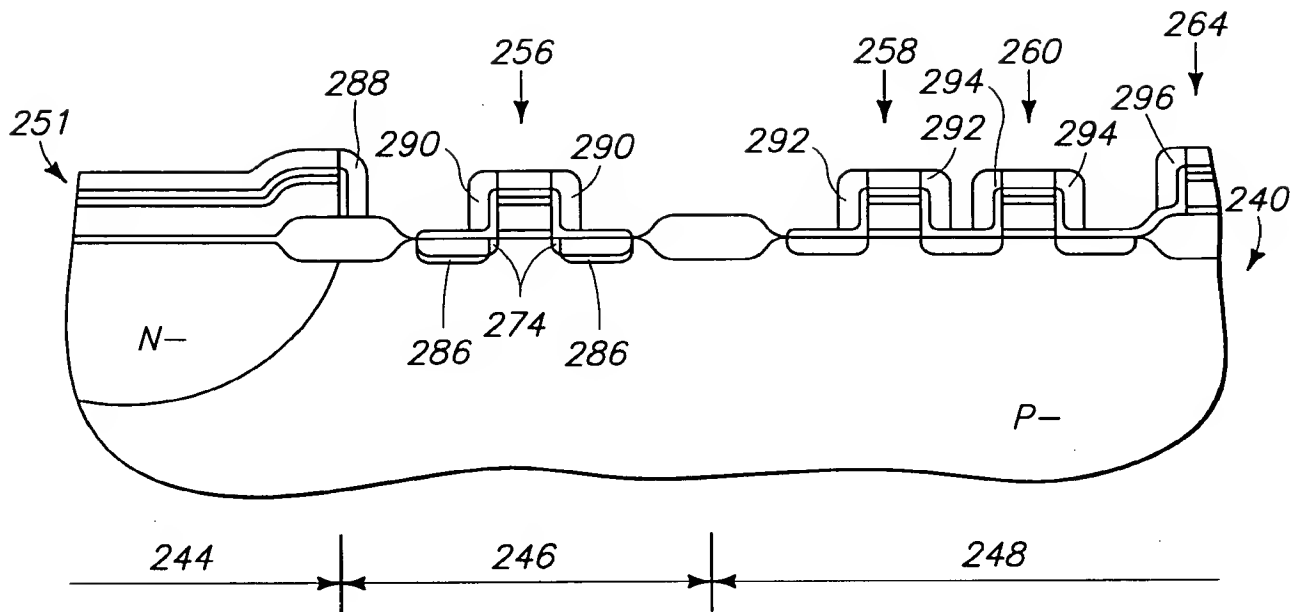


FIG. 11
PRIOR ART

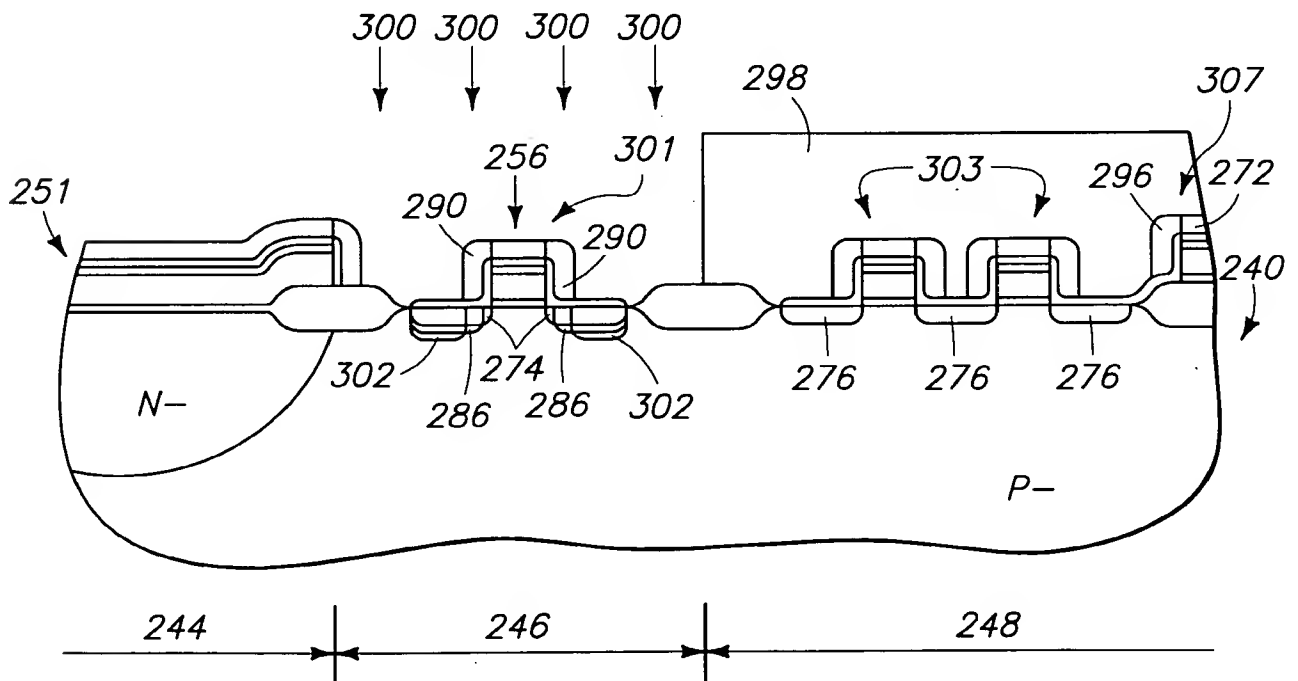


FIG. 12
PRIOR ART

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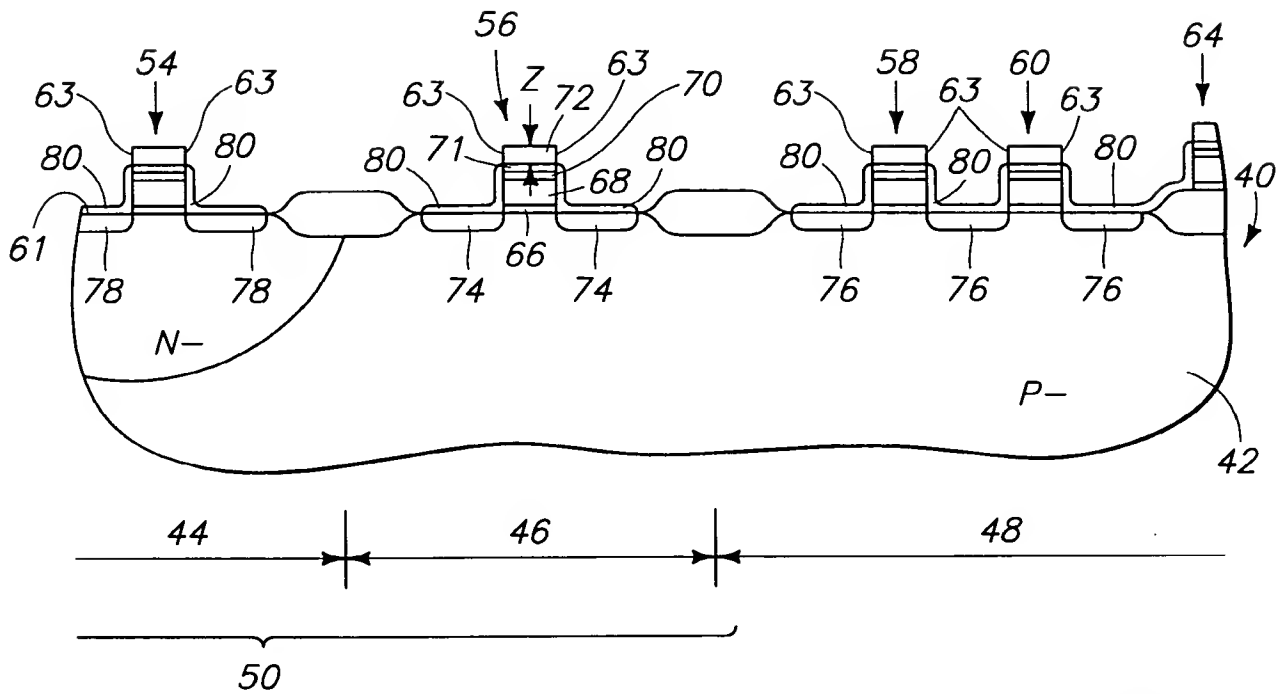


Fig. 1

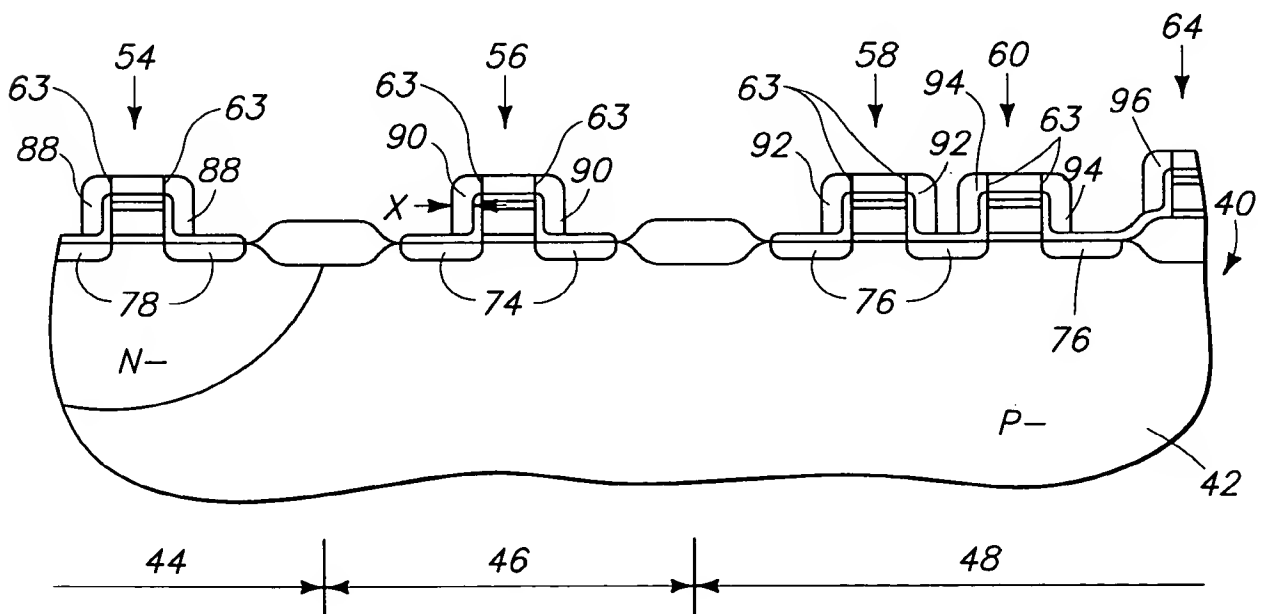


Fig. 2

FIG. 1

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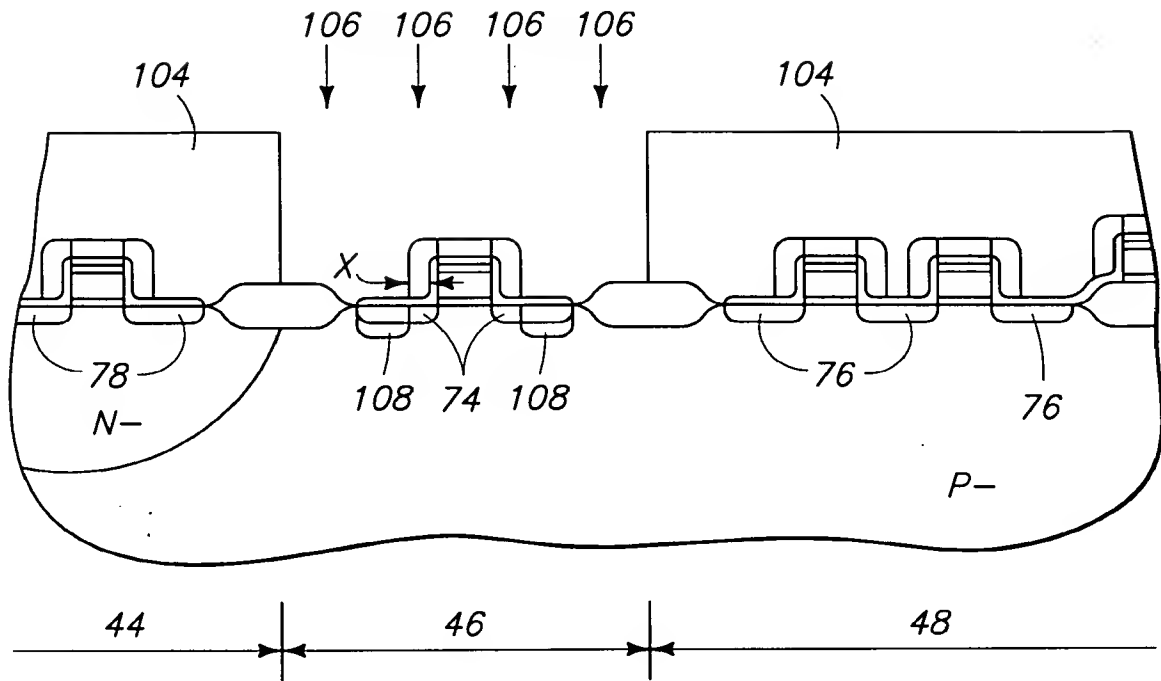


FIG. 15

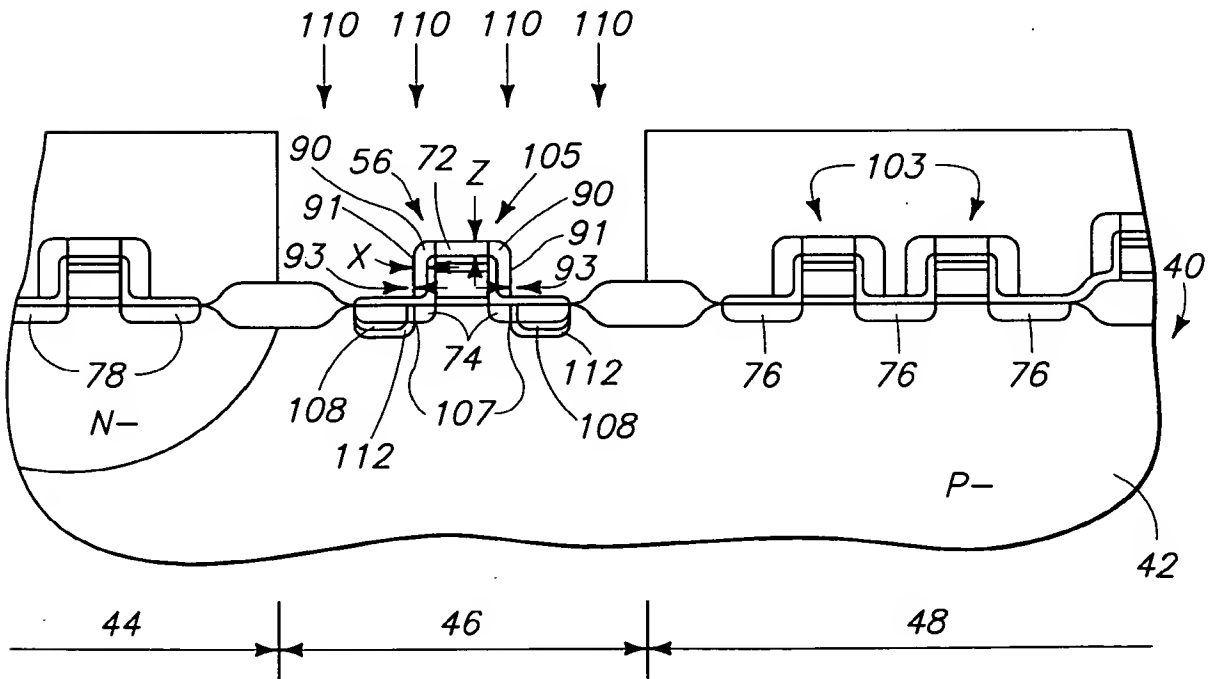


FIG. 16

FIG. 15

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FIG. 10

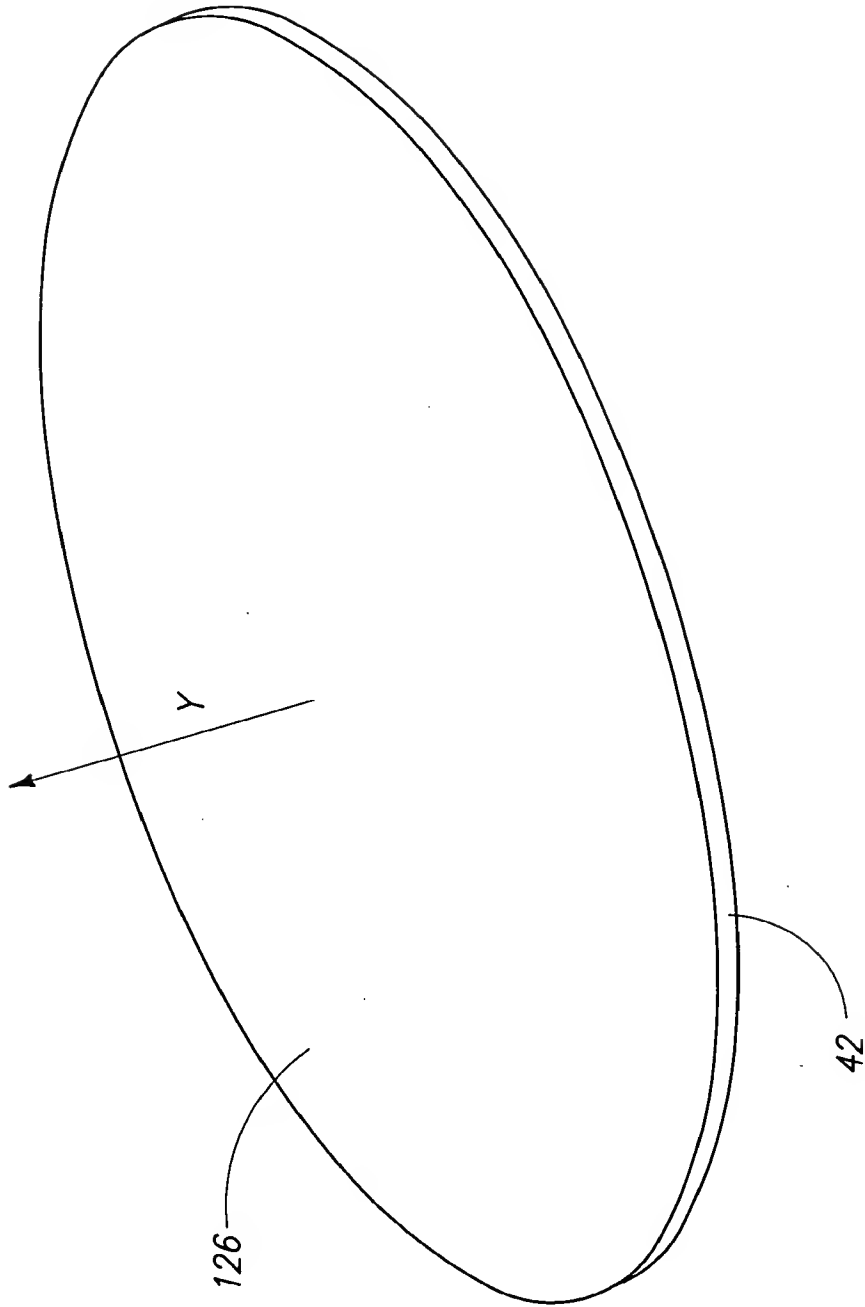


FIG. 11

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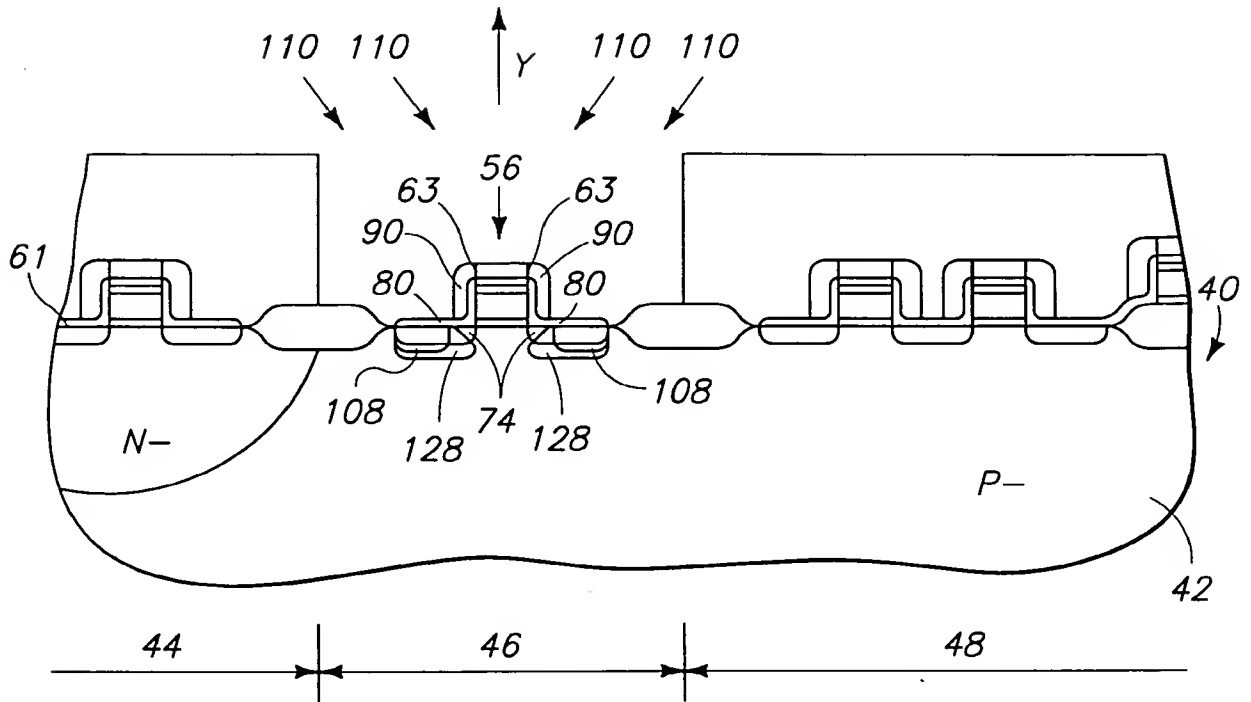


Fig. 11

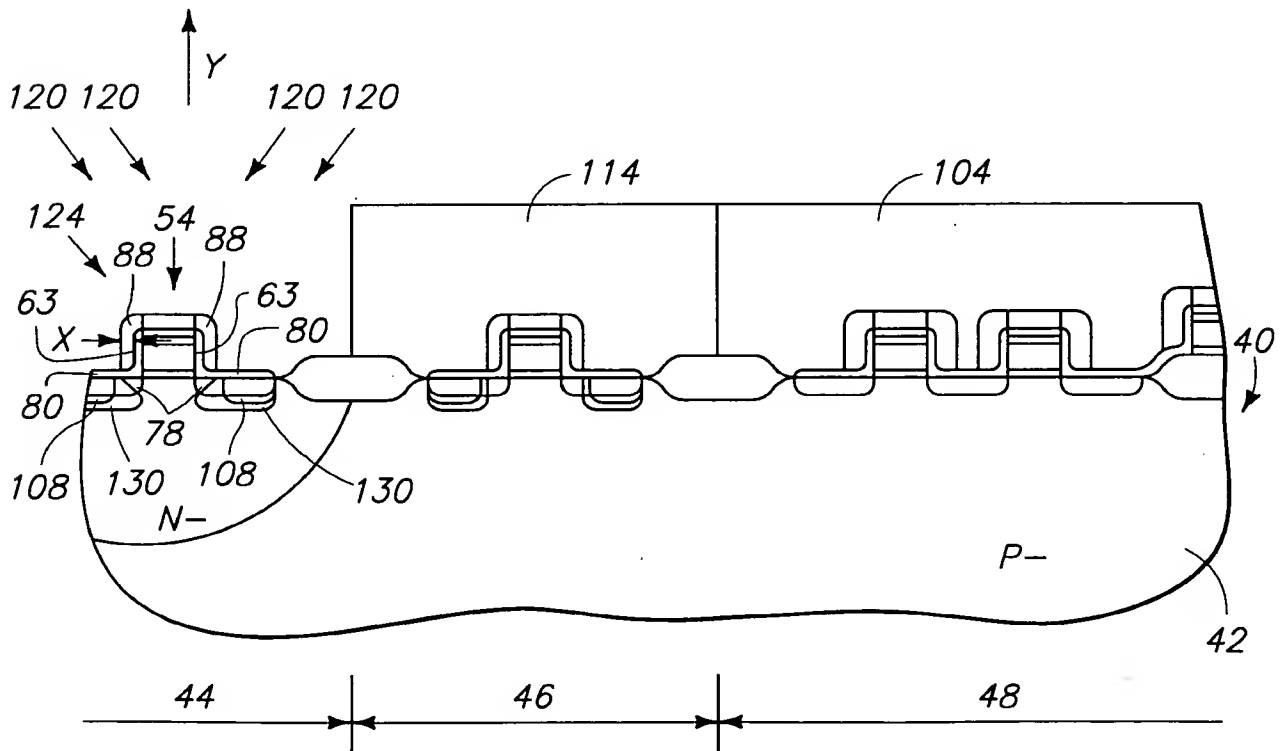


Fig. 12

Fig. 11

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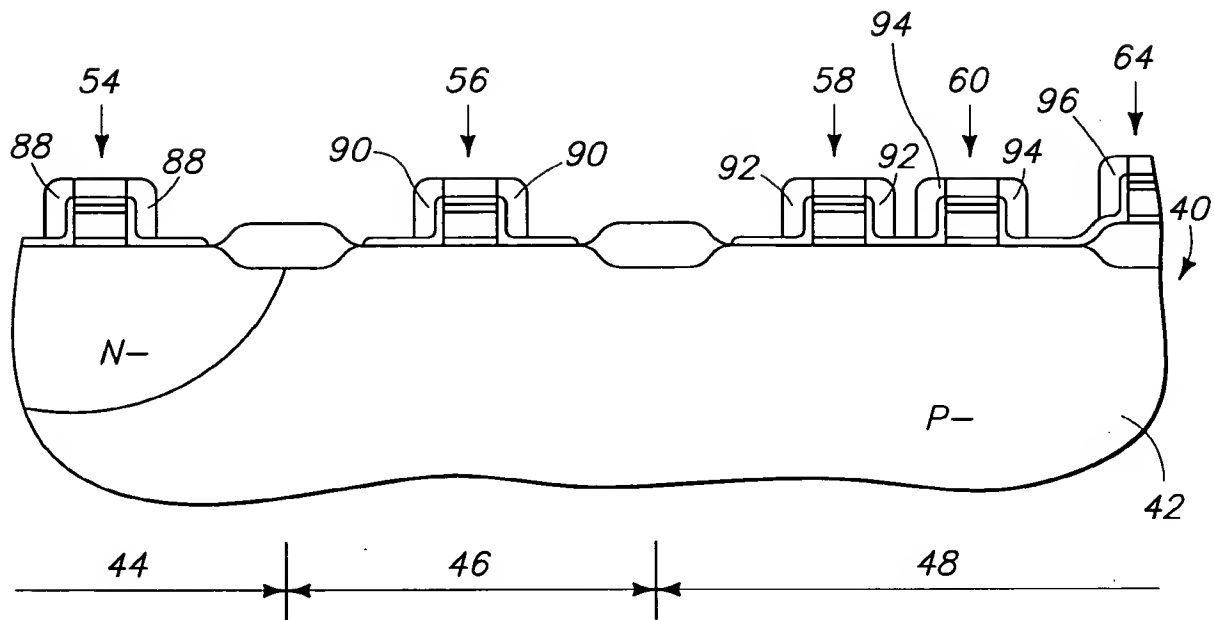


Fig. 22

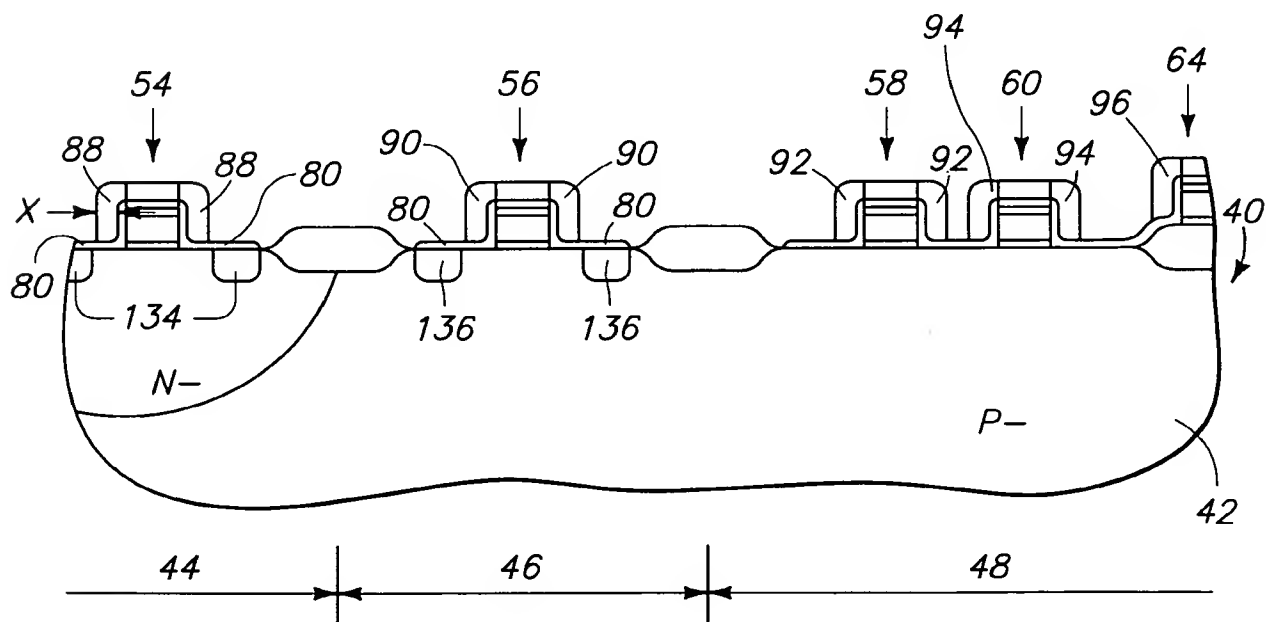
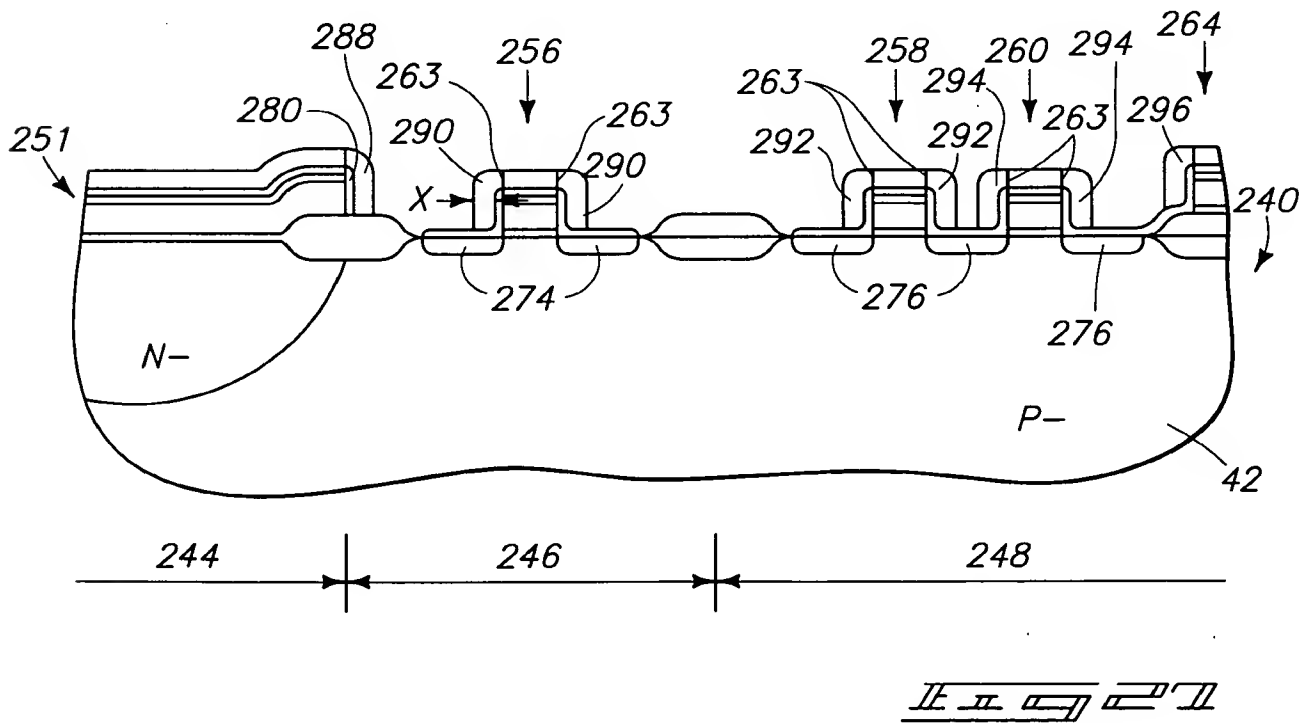
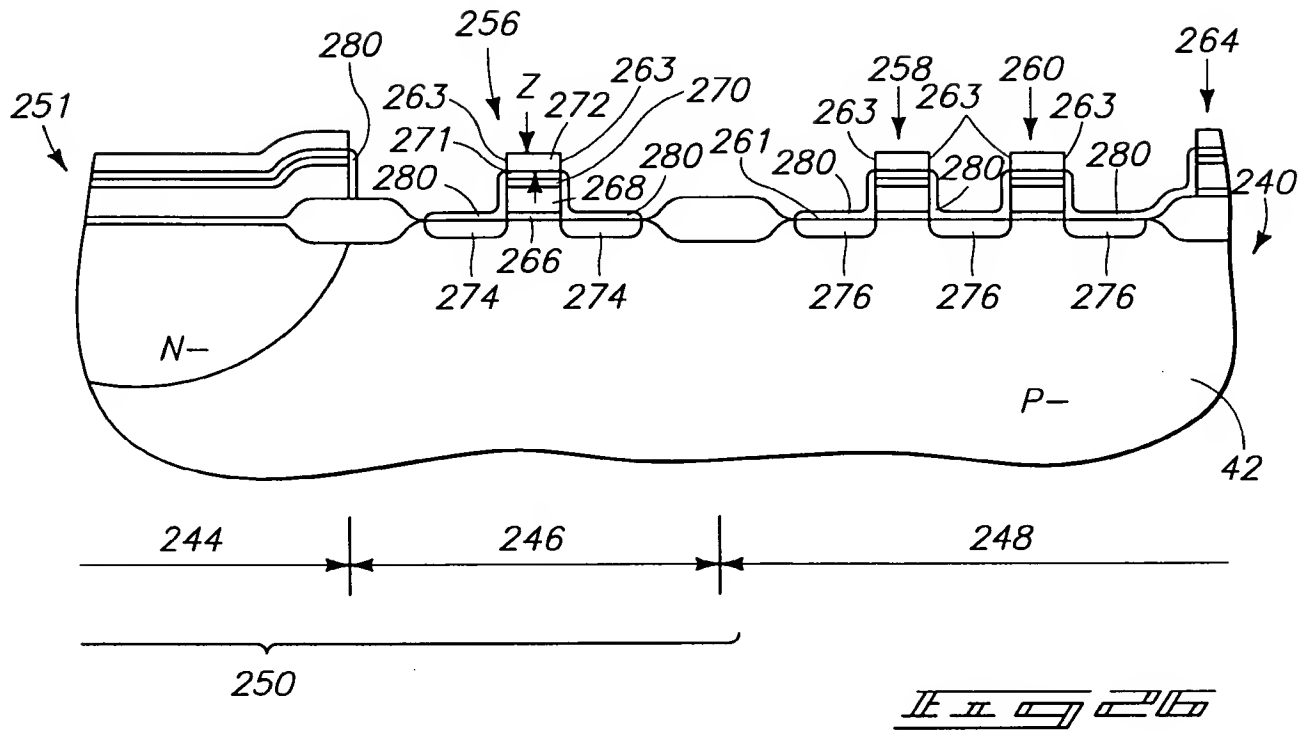


Fig. 23

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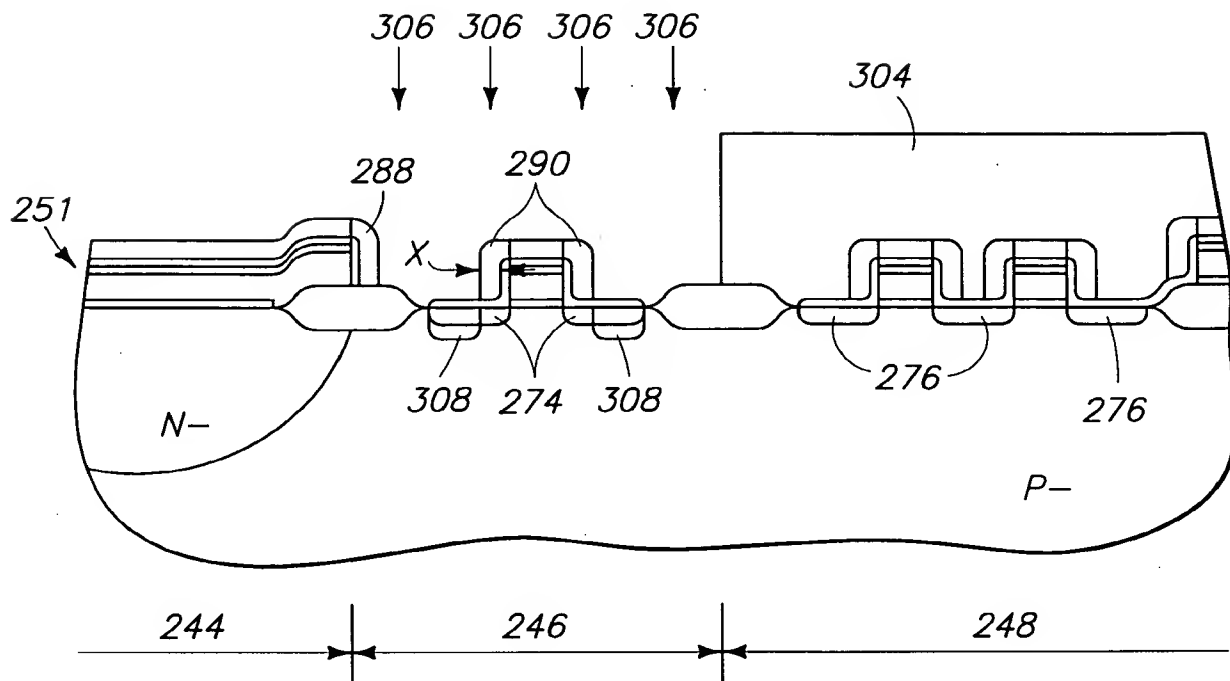


Fig. 28

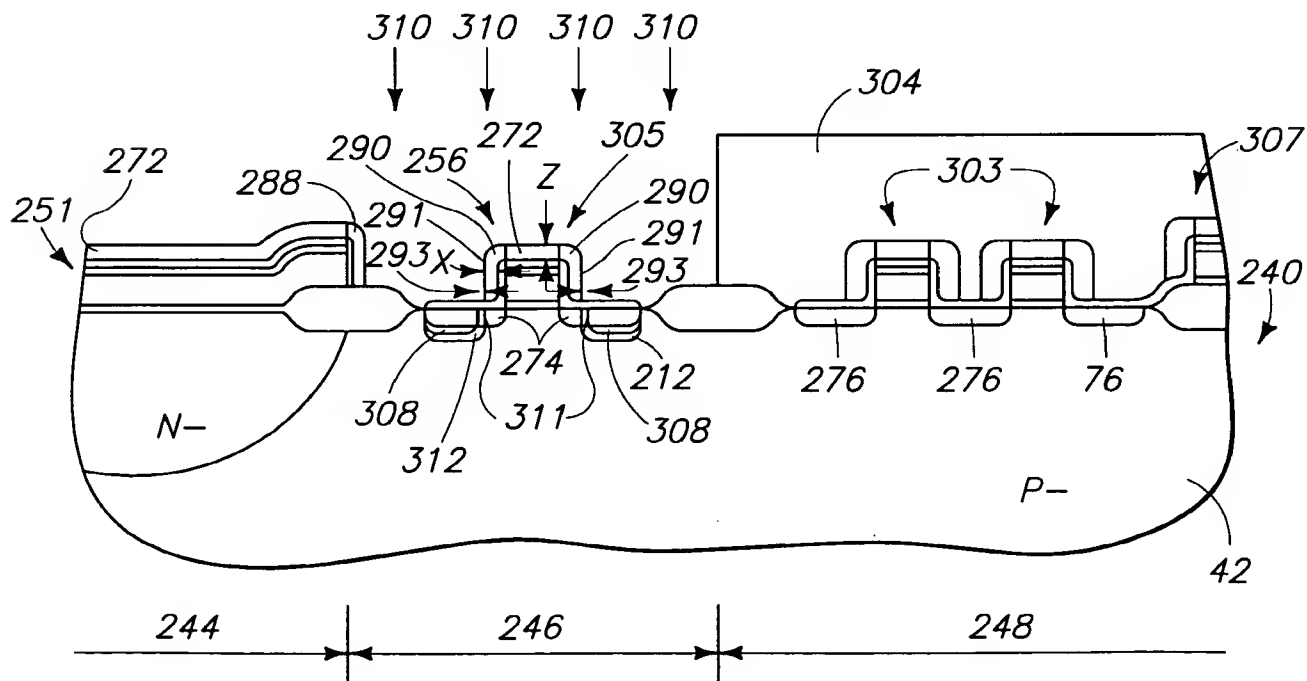


Fig. 29

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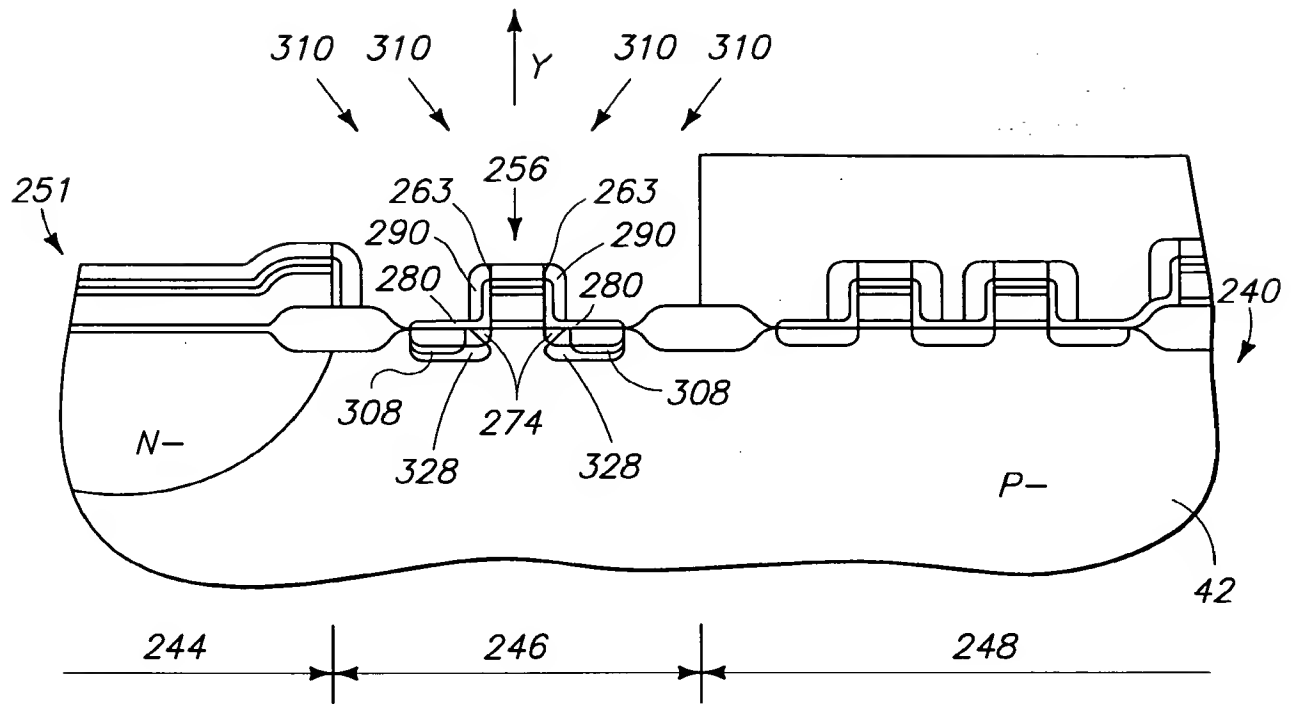


FIG. 16

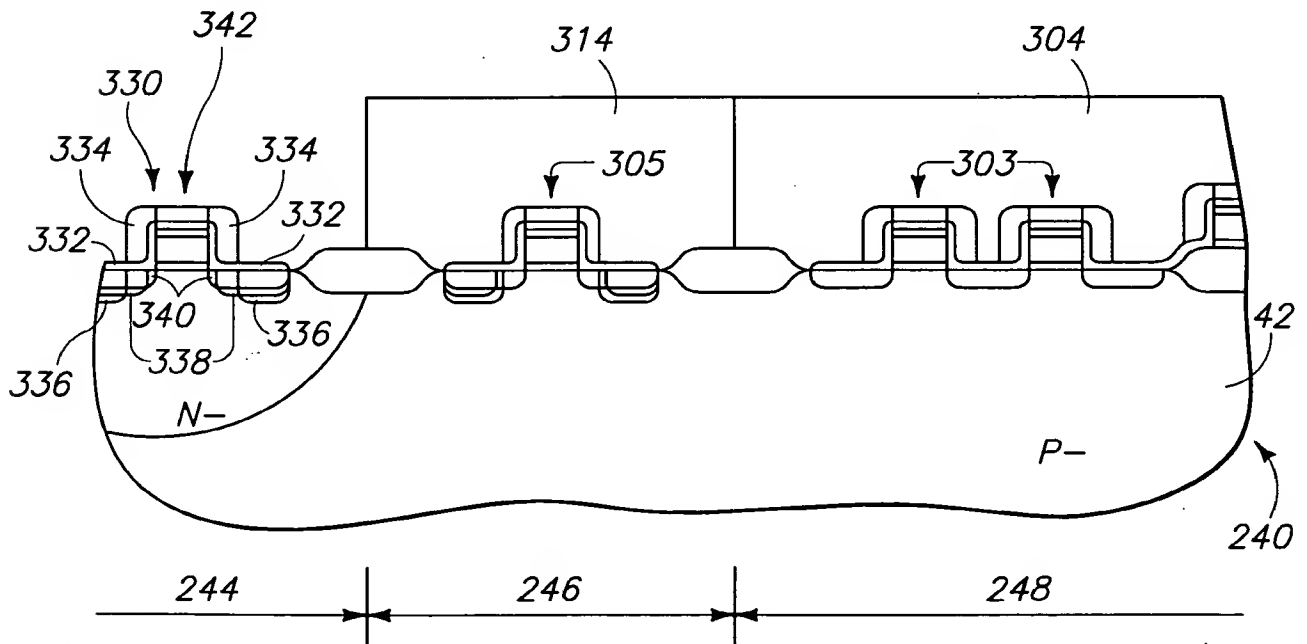


FIG. 17

FIG. 16

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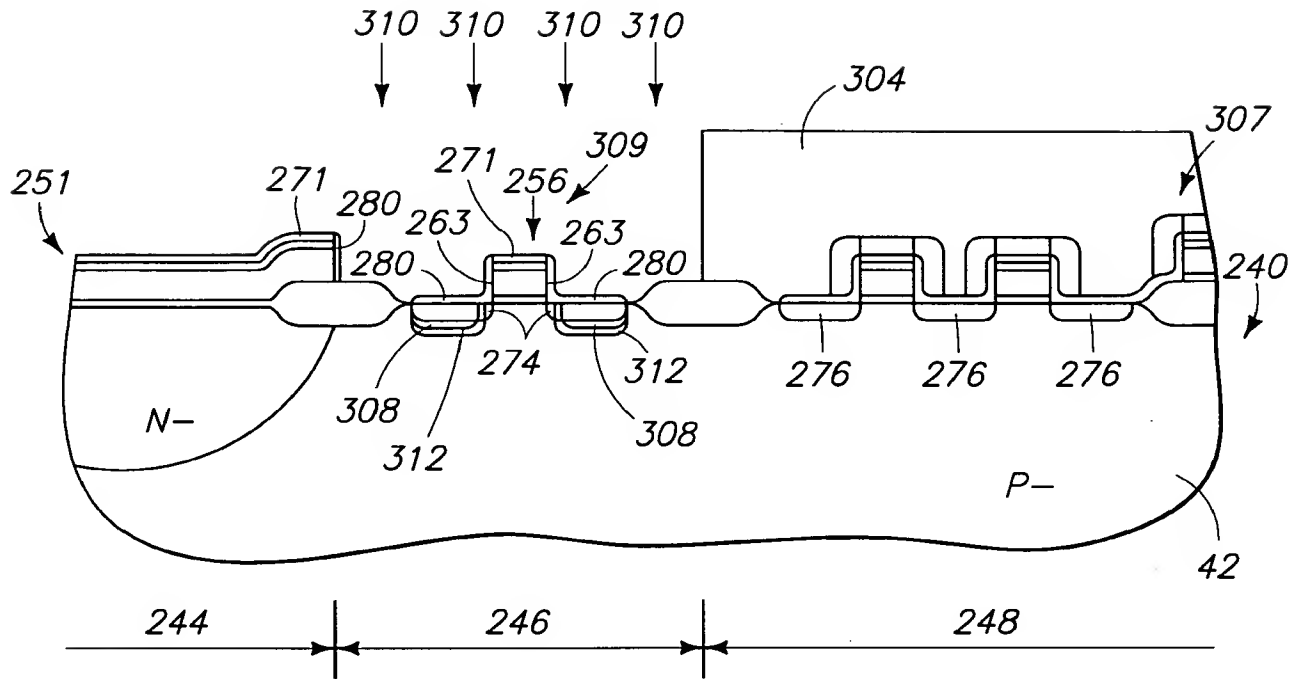


FIG. 17

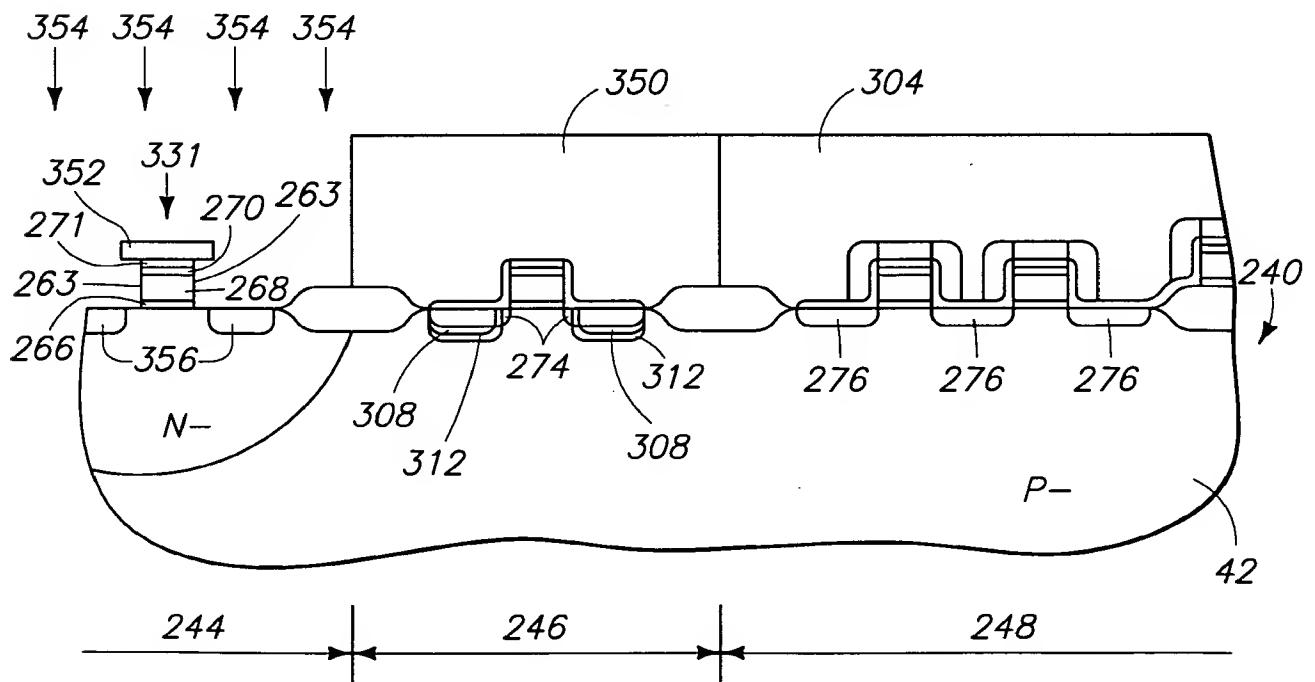


FIG. 18

FIG. 17



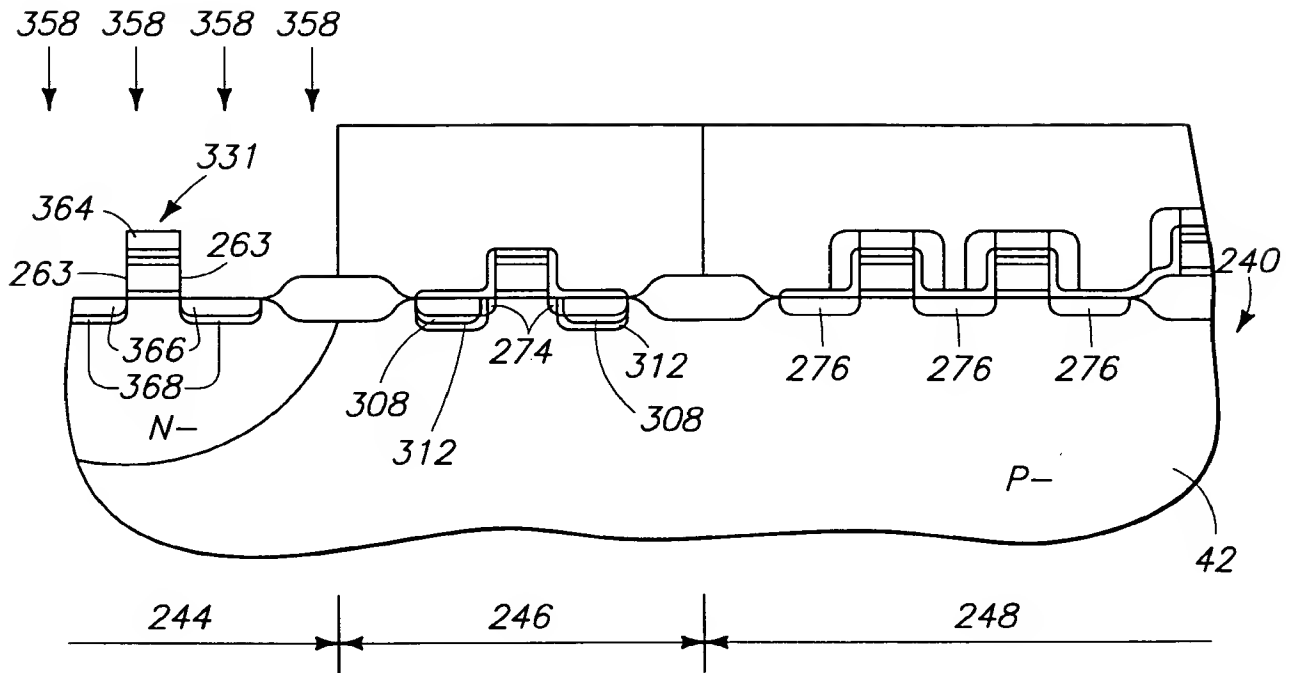


FIG. 16

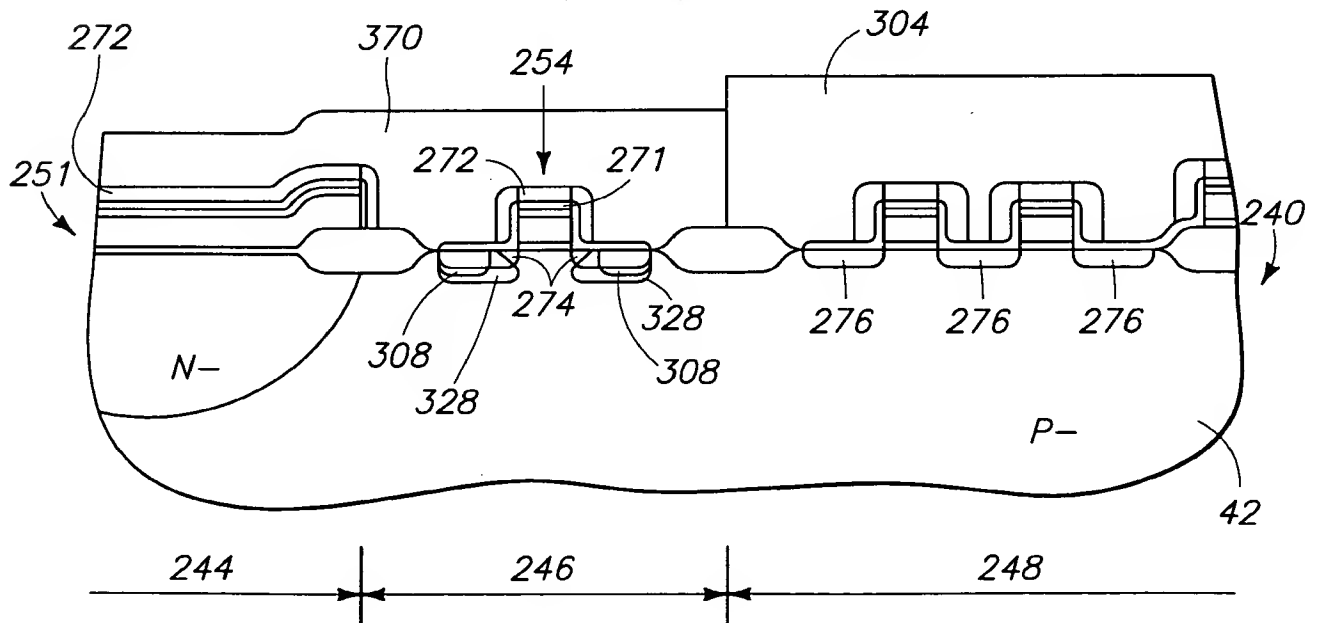


FIG. 17

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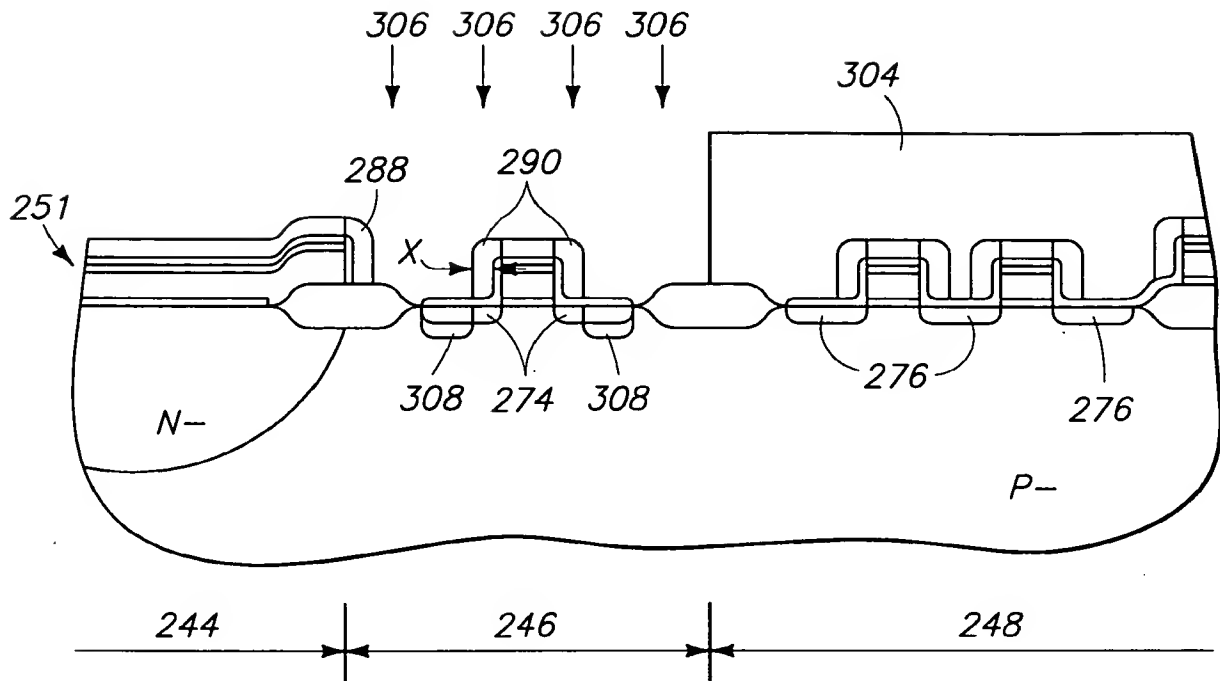


FIG. 28

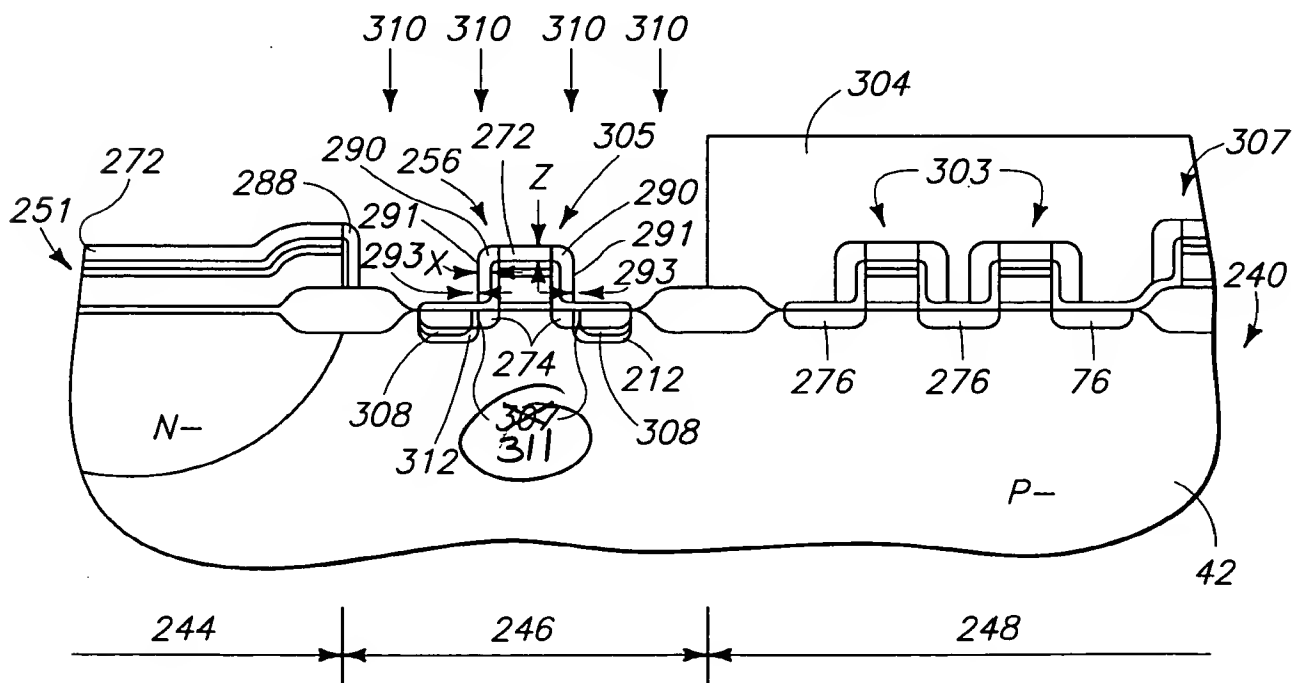


FIG. 29

FIG. 28

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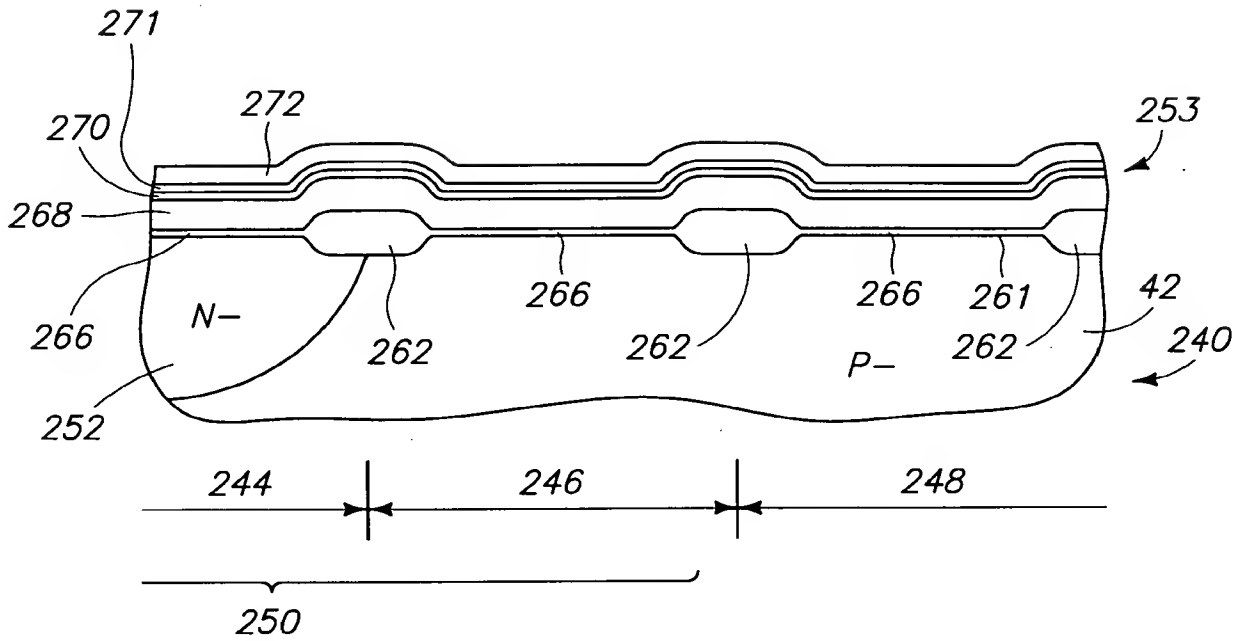


FIG. 1
PRIOR ART

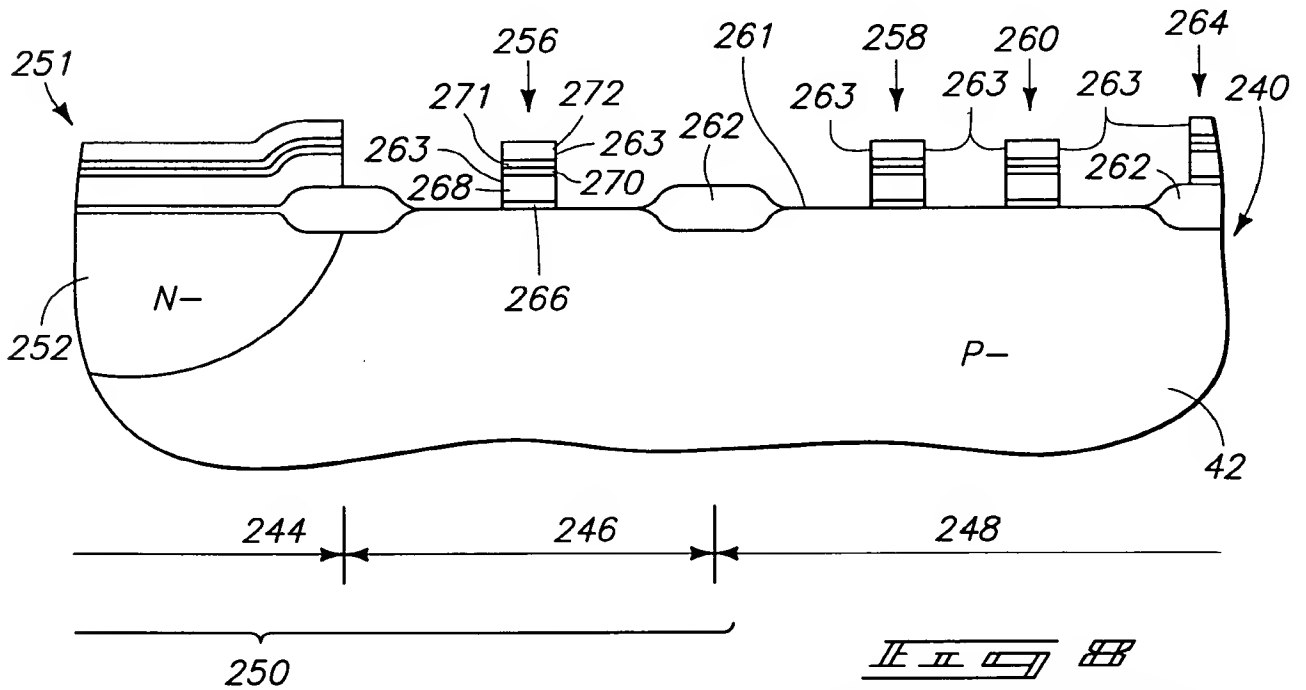


FIG. 2
PRIOR ART

FIG. 1

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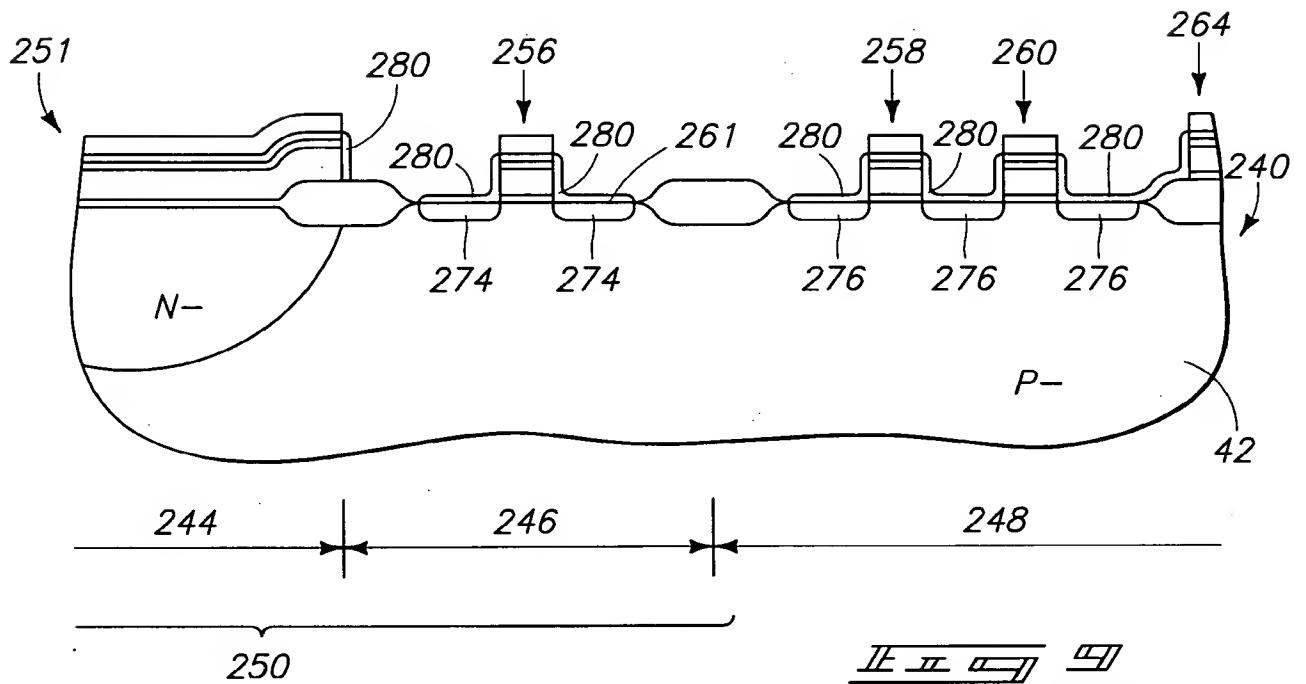


FIG. 9
PRIOR ART

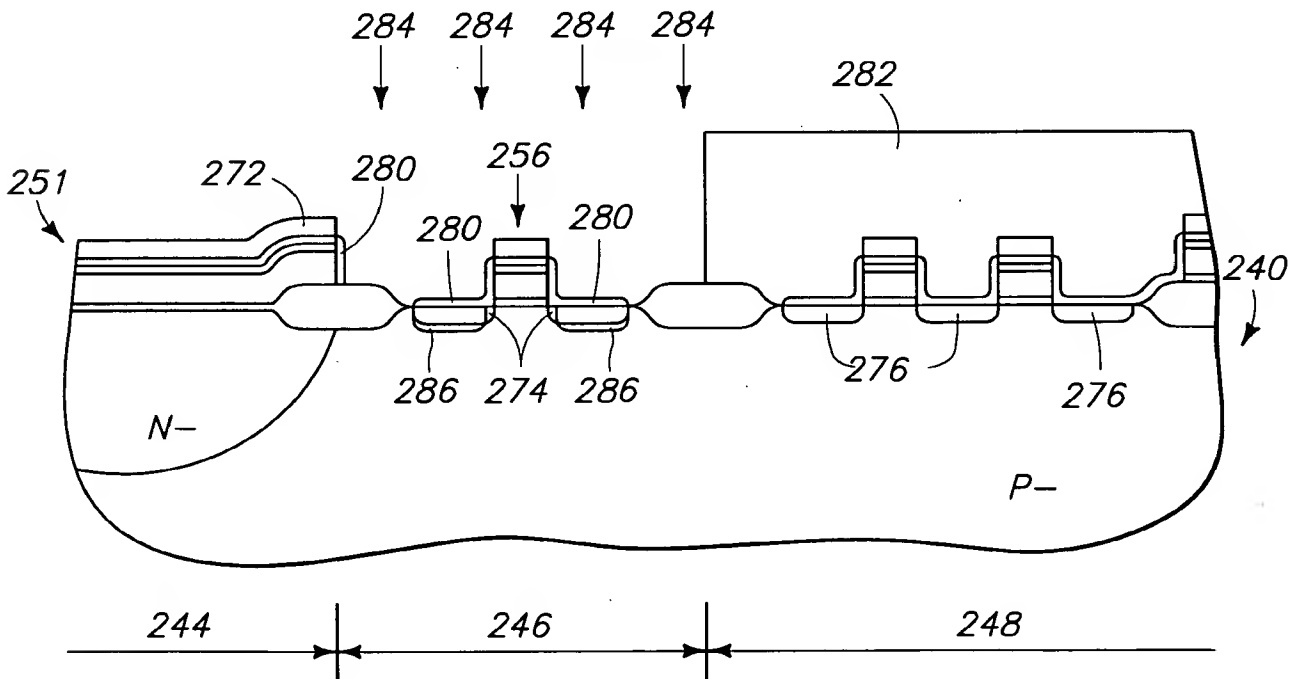


FIG. 10
PRIOR ART

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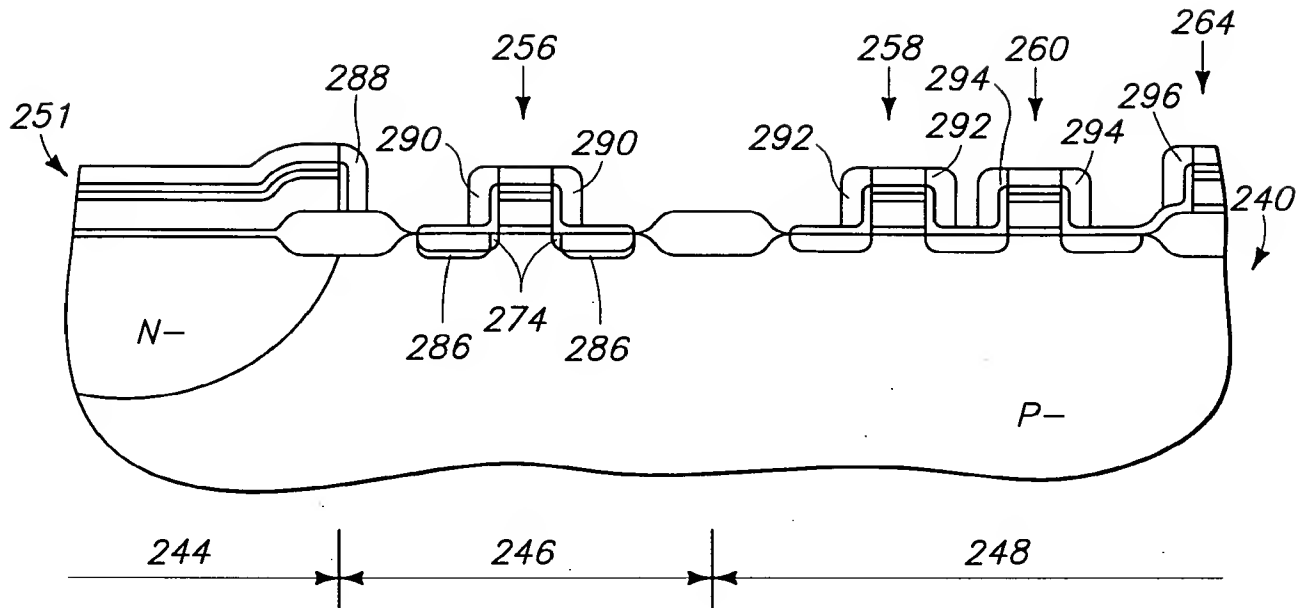


FIG. 1
PRIOR ART

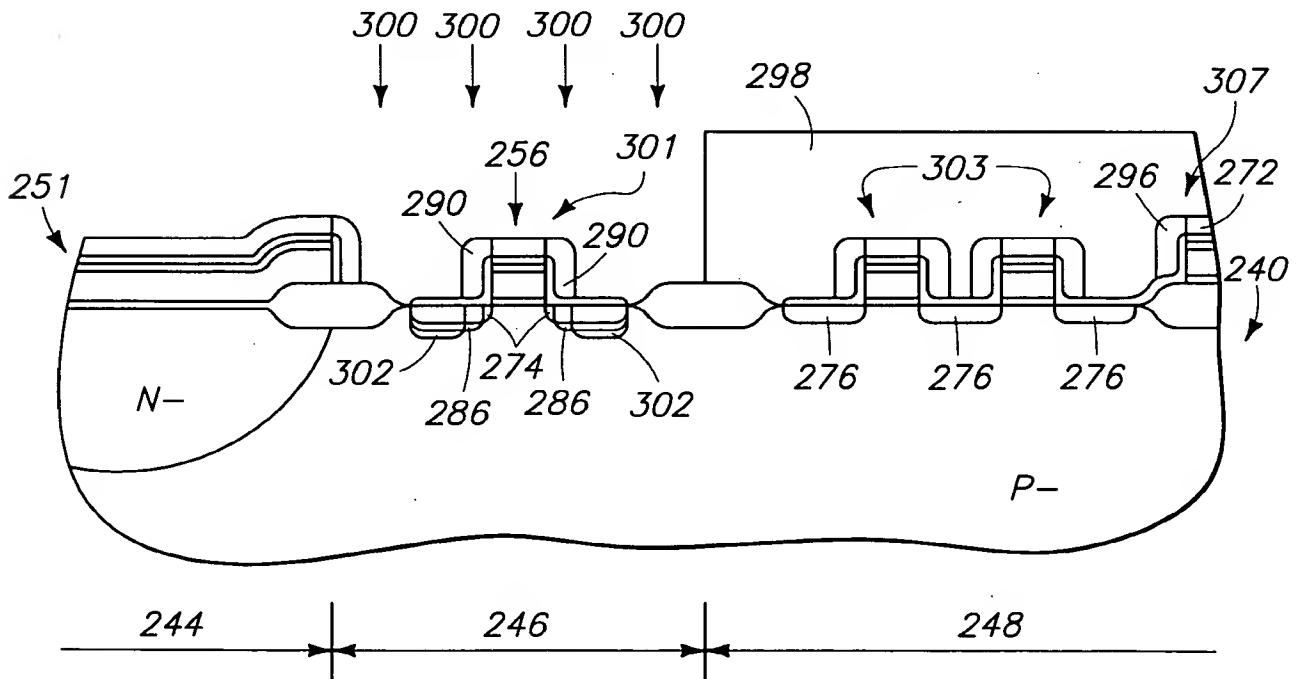


FIG. 2
PRIOR ART

FIG. 1

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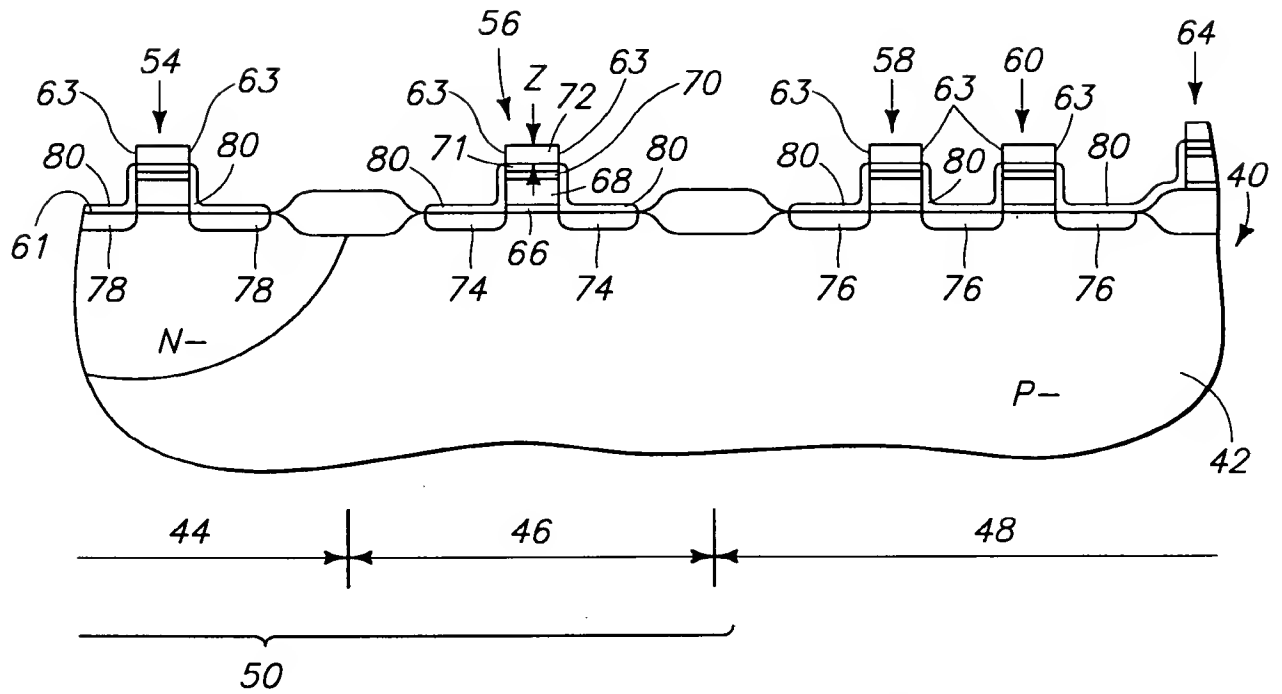


Fig. 7

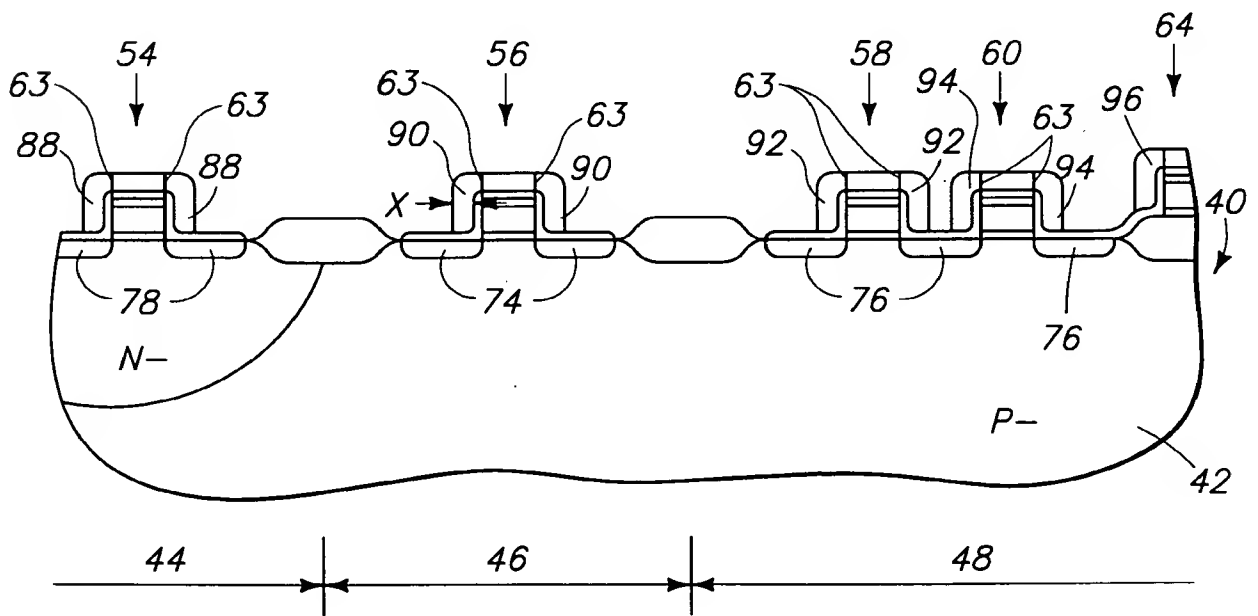
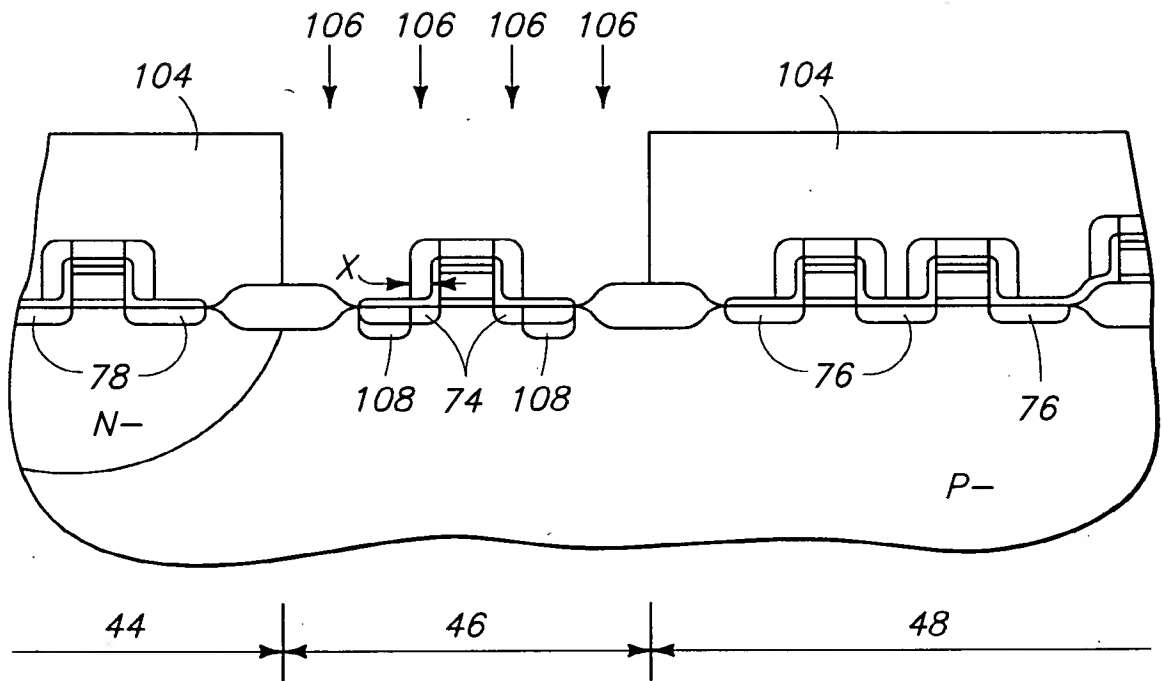


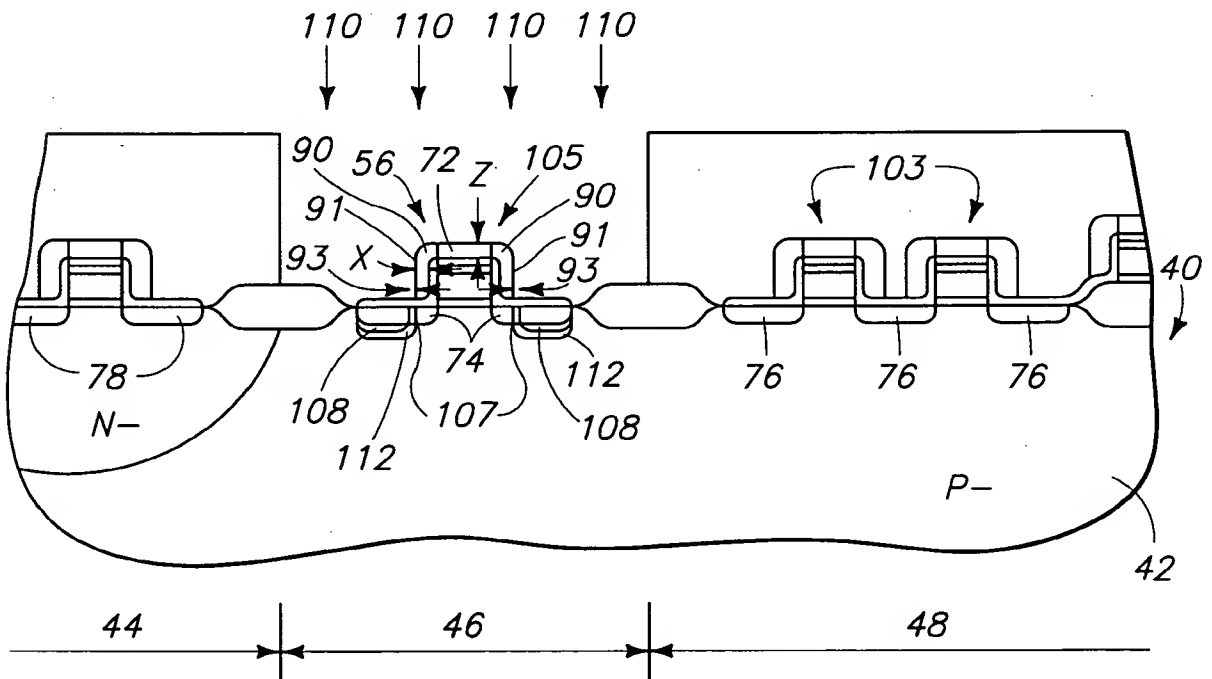
Fig. 8

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II II II II

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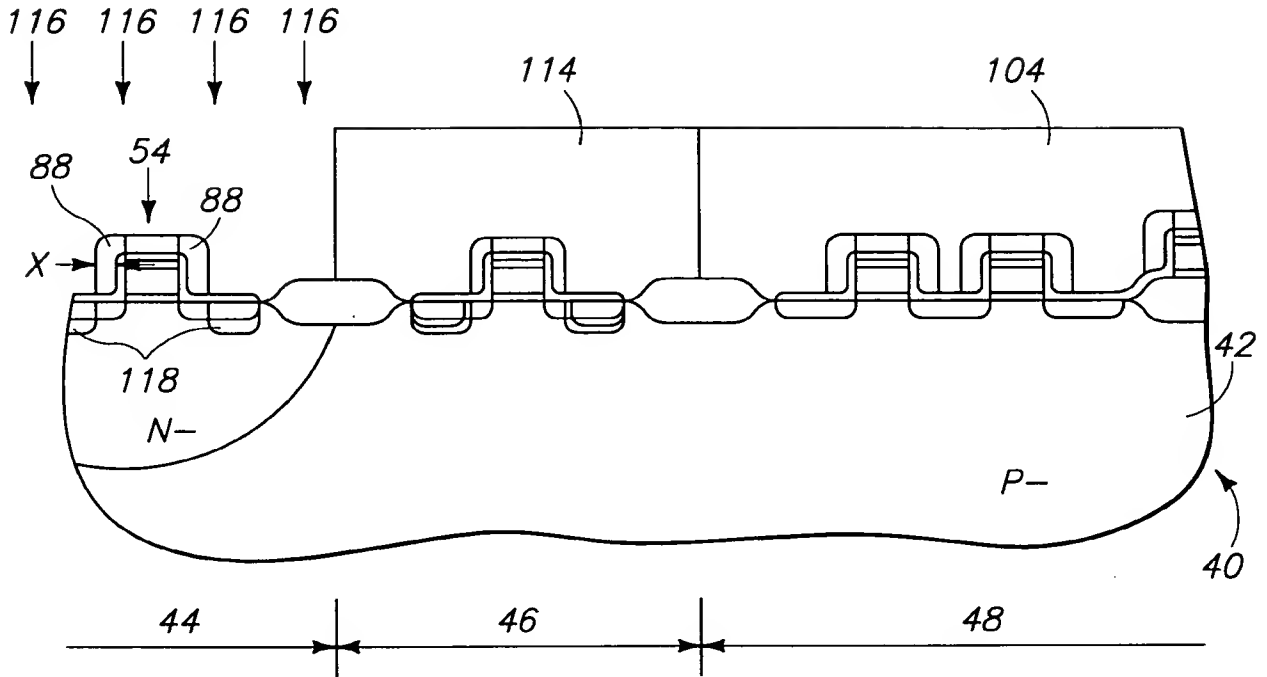


Fig. 1

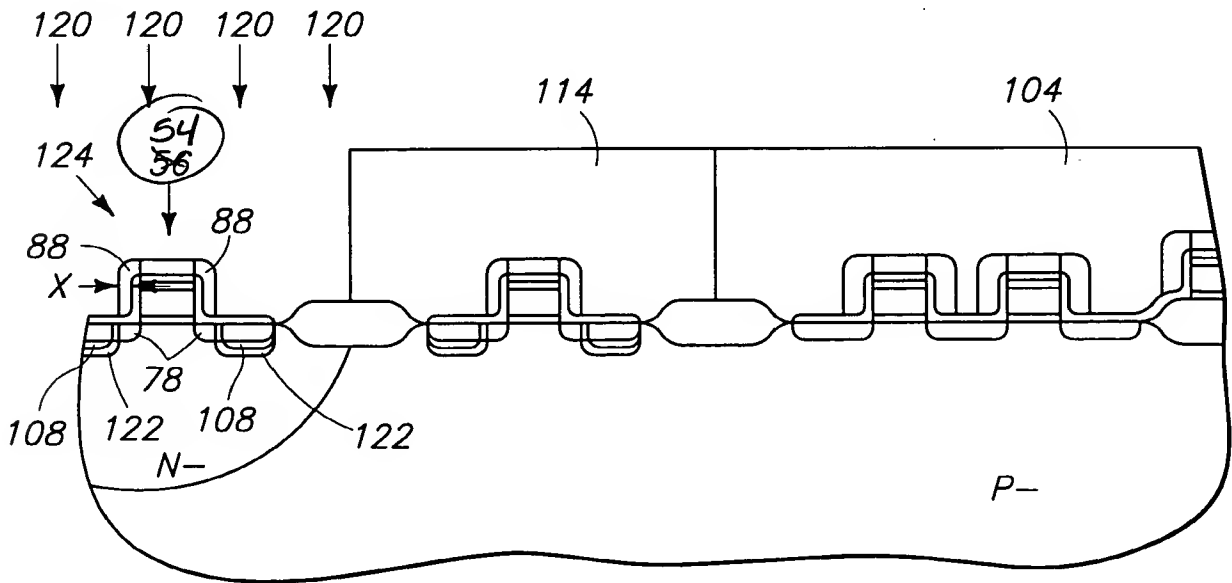


Fig. 2

FIG. 1

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FIG. 1

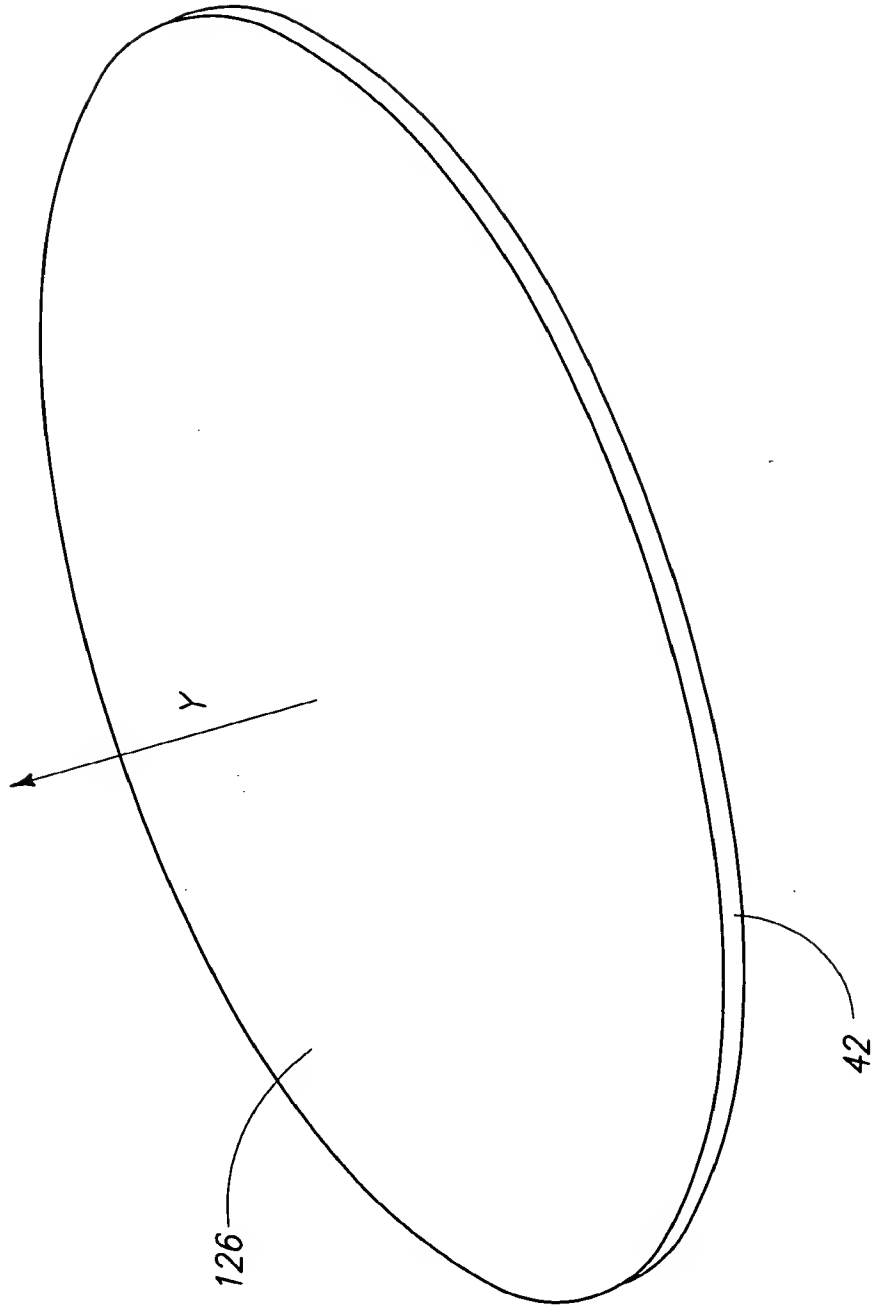
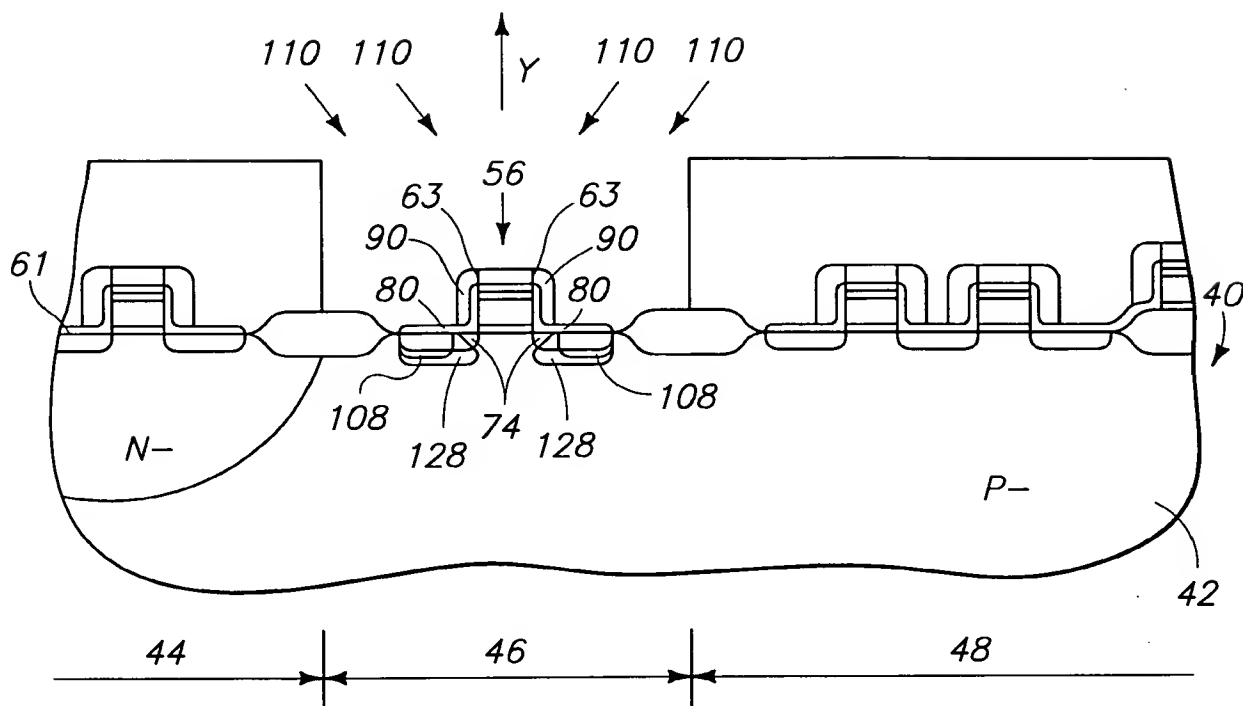
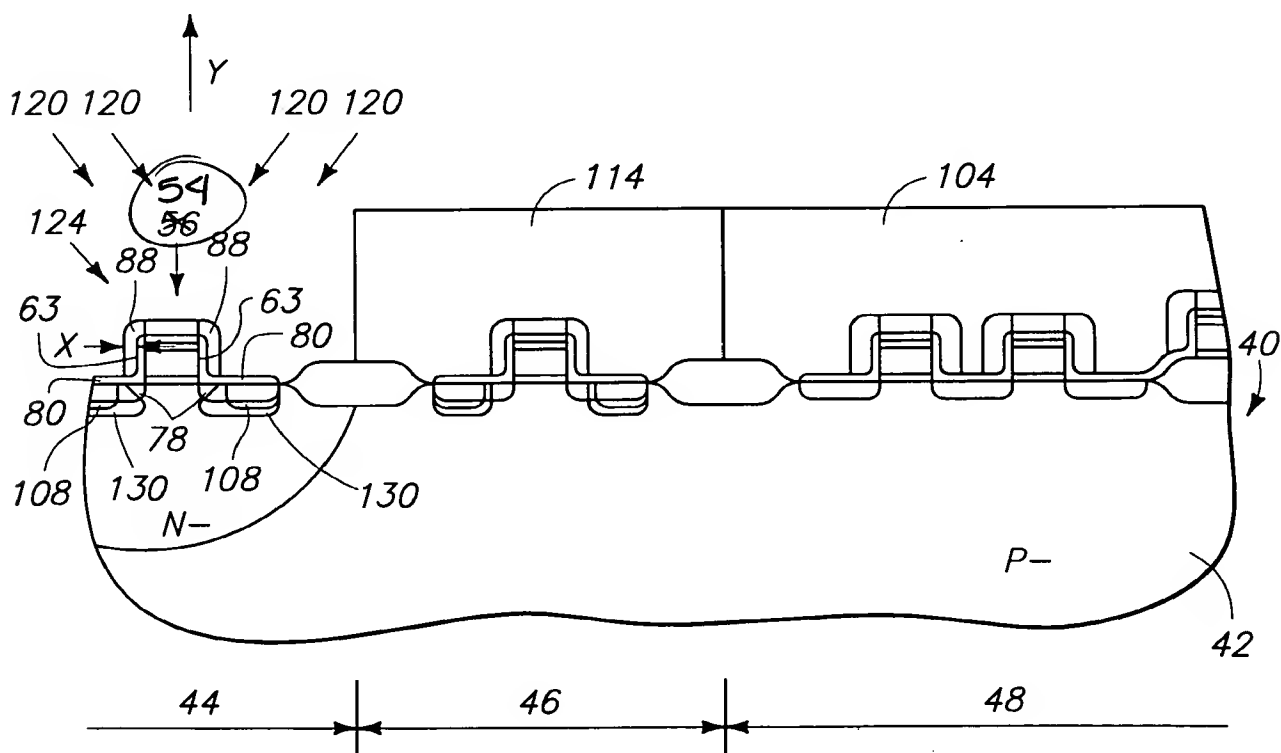


FIG. 2

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I-I



II-II

FIG. 1

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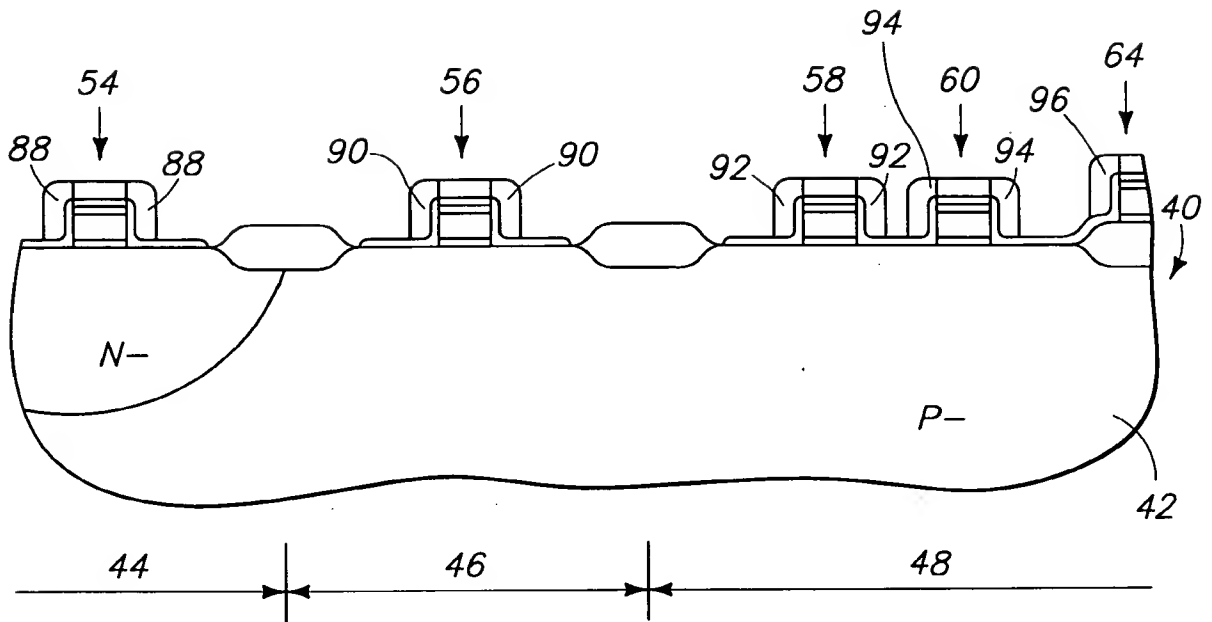


Fig. 22

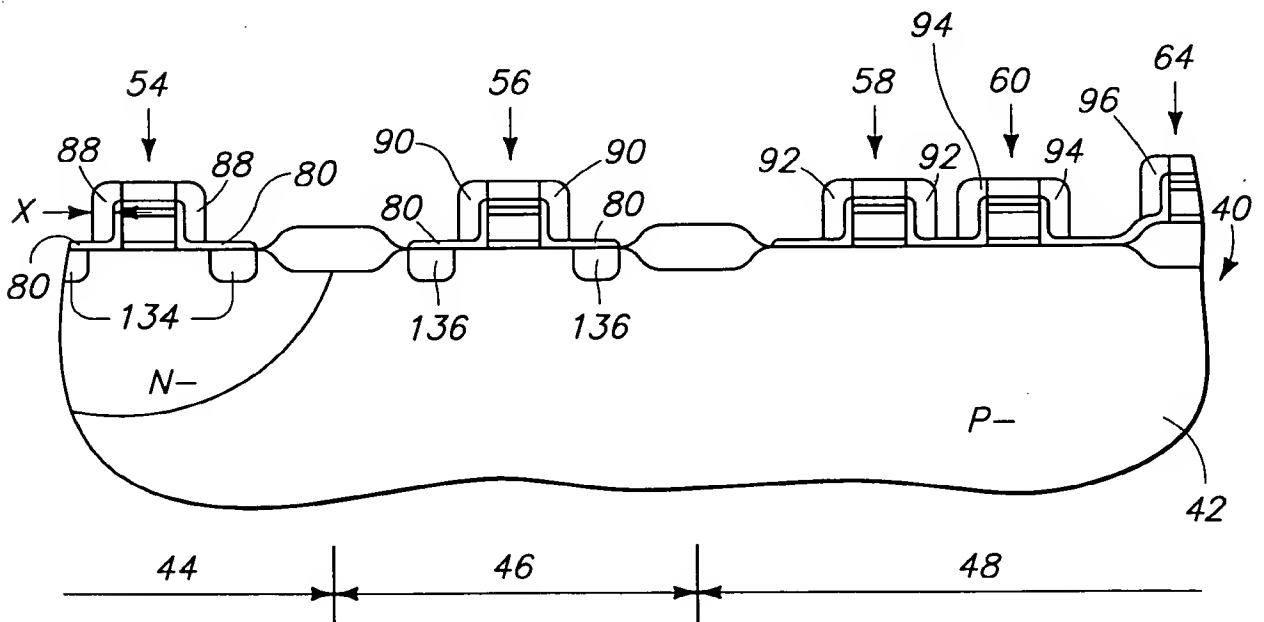


Fig. 23

FIG. 22

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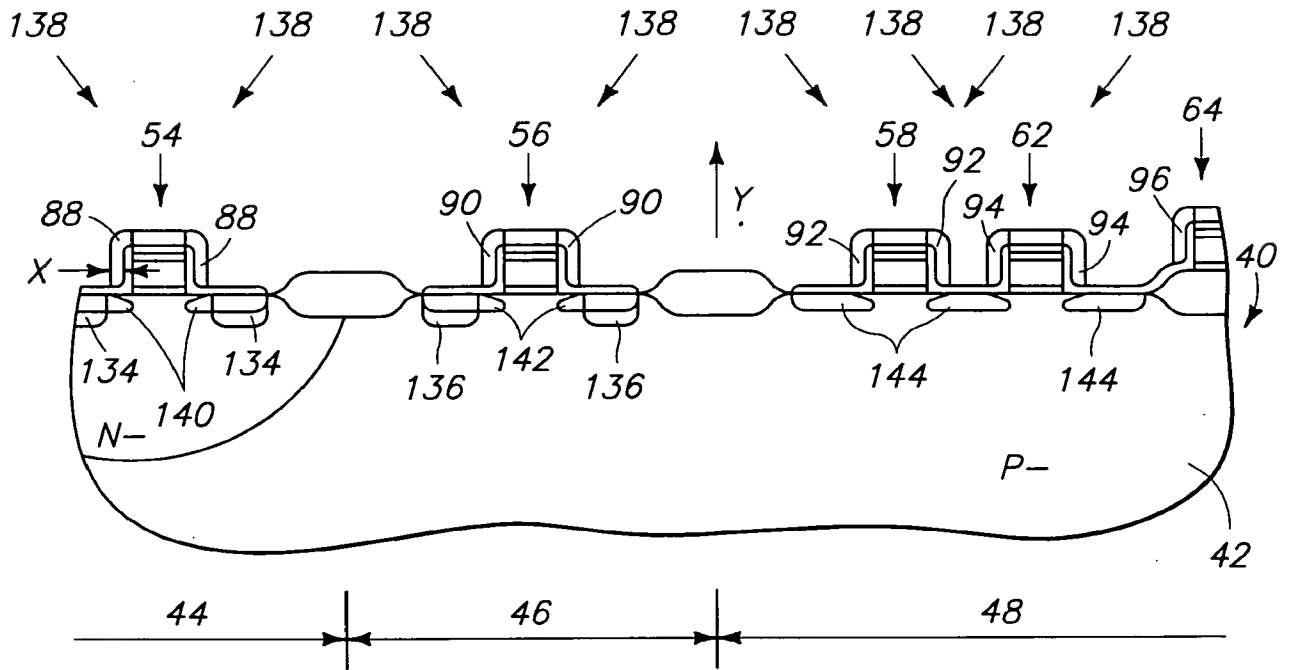


FIG. 2

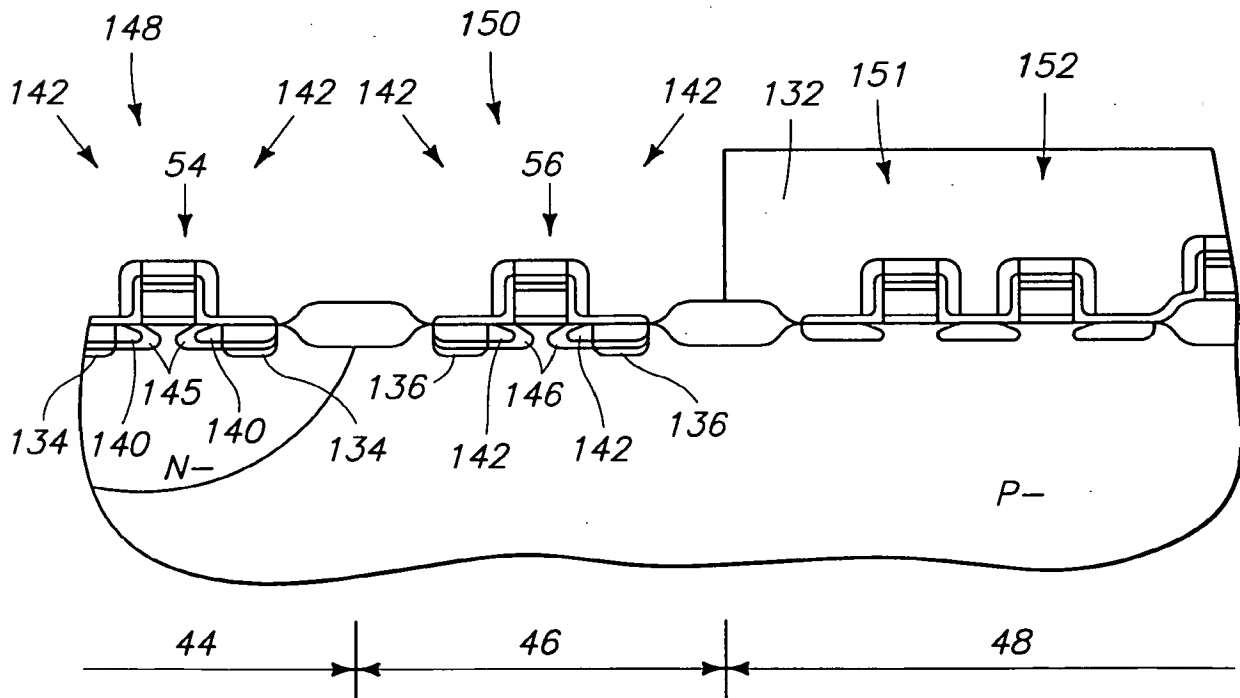


FIG. 3

FIG. 1

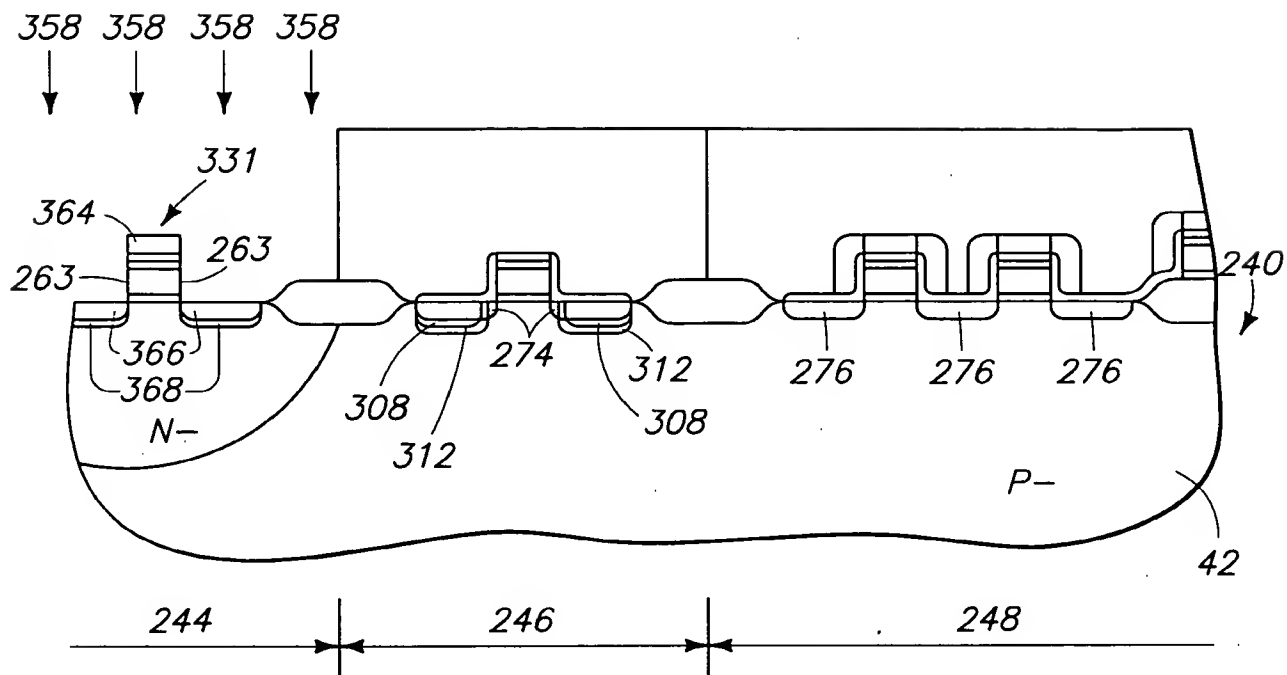


FIG. 19

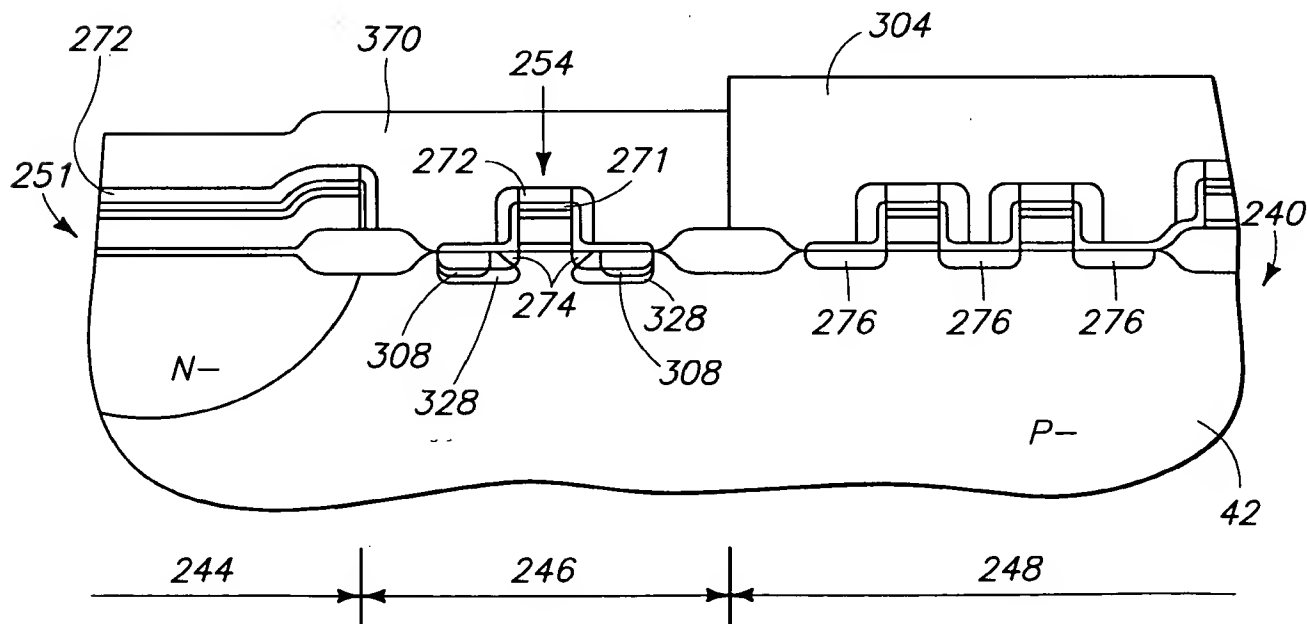


FIG. 20